BECKHOFF New Automation Technology

Documentation | EN

EP1518-0002

EtherCAT Box with 8 digital inputs and 2 counters



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1 Foreword

1.1 Notes on the documentation

Intended audience

This description is only intended for the use of trained specialists in control and automation engineering who are familiar with the applicable national standards.

It is essential that the documentation and the following notes and explanations are followed when installing and commissioning these components.

It is the duty of the technical personnel to use the documentation published at the respective time of each installation and commissioning.

The responsible staff must ensure that the application or use of the products described satisfy all the requirements for safety, including all the relevant laws, regulations, guidelines and standards.

Disclaimer

The documentation has been prepared with care. The products described are, however, constantly under development.

We reserve the right to revise and change the documentation at any time and without prior announcement.

No claims for the modification of products that have already been supplied may be made on the basis of the data, diagrams and descriptions in this documentation.

Trademarks

Beckhoff[®], TwinCAT[®], TwinCAT/BSD[®], TC/BSD[®], EtherCAT[®], EtherCAT G[®], EtherCAT G10[®], EtherCAT P[®], Safety over EtherCAT[®], TwinSAFE[®], XFC[®], XTS[®] and XPlanar[®] are registered trademarks of and licensed by Beckhoff Automation GmbH. Other designations used in this publication may be trademarks whose use by third parties for their own purposes could violate the rights of the owners.

Patent Pending

The EtherCAT Technology is covered, including but not limited to the following patent applications and patents: EP1590927, EP1789857, EP1456722, EP2137893, DE102015105702 with corresponding applications or registrations in various other countries.



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1.2 Safety instructions

Safety regulations

Please note the following safety instructions and explanations! Product-specific safety instructions can be found on following pages or in the areas mounting, wiring, commissioning etc.

Exclusion of liability

All the components are supplied in particular hardware and software configurations appropriate for the application. Modifications to hardware or software configurations other than those described in the documentation are not permitted, and nullify the liability of Beckhoff Automation GmbH & Co. KG.

Personnel qualification

This description is only intended for trained specialists in control, automation and drive engineering who are familiar with the applicable national standards.

Description of instructions

In this documentation the following instructions are used. These instructions must be read carefully and followed without fail!

▲ DANGER

Serious risk of injury!

Failure to follow this safety instruction directly endangers the life and health of persons.

WARNING

Risk of injury!

Failure to follow this safety instruction endangers the life and health of persons.

Personal injuries!

Failure to follow this safety instruction can lead to injuries to persons.

NOTE

Damage to environment/equipment or data loss

Failure to follow this instruction can lead to environmental damage, equipment damage or data loss.



Tip or pointer

This symbol indicates information that contributes to better understanding.

1.3 Documentation Issue Status

Version	Comment
2.4	Chapter "Supply voltages" updated
2.3	Technical data updated
	UL requirements updated
	Dimensions updated
2.2	Front page updated
	Scope of delivery added
	Structure update
2.1.0	Safety instructions new layout
	Chapter <i>Mounting</i> updated
2.0.0	Migration
	Basic function principles chapter corrected
1.1.0	Power Connection updated
1.0.0	First release

Firmware and hardware versions

This documentation refers to the firmware and hardware version that was applicable at the time the documentation was written.

The module features are continuously improved and developed further. Modules having earlier production statuses cannot have the same properties as modules with the latest status. However, existing properties are retained and are not changed, so that older modules can always be replaced with new ones.

Documentation Version	Firmware	Hardware
2.4	04	11
2.3	04	09
2.2	04	08
2.1	04	06
2.0.0	04	05
1.0.0	02	00

The firmware and hardware version (delivery state) can be found in the batch number (D-number) printed on the side of the EtherCAT Box.

Syntax of the batch number (D-number)

WW YY FF HH

WW - week of production (calendar week) YY - year of production FF - firmware version HH - hardware version

Example with D no.: 55 09 01 00:

55 - week of production 55

09 - year of production 2009

01 - firmware version 01

00 - hardware version 001

Further information on this topic: <u>Version identification of EtherCAT devices [> 58]</u>.

2 EtherCAT Box - Introduction

The EtherCAT system has been extended with EtherCAT Box modules with protection class IP67. Through the integrated EtherCAT interface the modules can be connected directly to an EtherCAT network without an additional Coupler Box. The high-performance of EtherCAT is thus maintained into each module.

The extremely low dimensions of only $126 \times 30 \times 26.5 \text{ mm}$ (h x w x d) are identical to those of the Fieldbus Box extension modules. They are thus particularly suitable for use where space is at a premium. The small mass of the EtherCAT modules facilitates applications with mobile I/O interface (e.g. on a robot arm). The EtherCAT connection is established via screened M8 connectors.

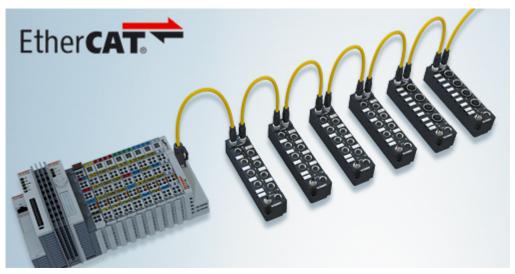


Fig. 1: EtherCAT Box Modules within an EtherCAT network

The robust design of the EtherCAT Box modules enables them to be used directly at the machine. Control cabinets and terminal boxes are now no longer required. The modules are fully sealed and therefore ideally prepared for wet, dirty or dusty conditions.

Pre-assembled cables significantly simplify EtherCAT and signal wiring. Very few wiring errors are made, so that commissioning is optimized. In addition to pre-assembled EtherCAT, power and sensor cables, field-configurable connectors and cables are available for maximum flexibility. Depending on the application, the sensors and actuators are connected through M8 or M12 connectors.

The EtherCAT modules cover the typical range of requirements for I/O signals with protection class IP67:

- digital inputs with different filters (3.0 ms or 10 μs)
- digital outputs with 0.5 or 2 A output current
- analog inputs and outputs with 16 bit resolution
- Thermocouple and RTD inputs
- Stepper motor modules

XFC (eXtreme Fast Control Technology) modules, including inputs with time stamp, are also available.

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Fig. 2: EtherCAT Box with M8 connections for sensors/actuators



Fig. 3: EtherCAT Box with M12 connections for sensors/actuators

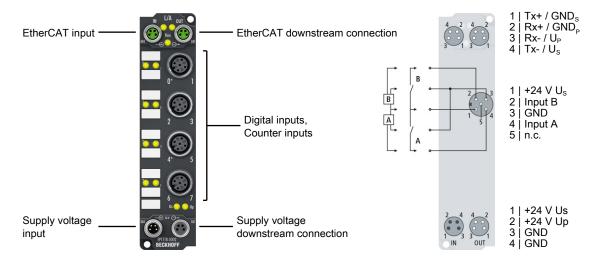
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Basic EtherCAT documentation

You will find a detailed description of the EtherCAT system in the Basic System Documentation for EtherCAT, which is available for download from our website (www.beckhoff.com) under Downloads.

3 **Product overview**

3.1 Introduction



8 digital inputs (24 $V_{\mbox{\tiny DC}}$), 2 counters

The EP1518-0002 EtherCAT Box with digital inputs acquires binary control signals from the process level and transfers them, with electrical isolation, to the controller.

The signal state is indicated by means of light emitting diodes. Connection is via M12 connectors. The input filters can be set between 0 and 100 ms via EtherCAT.

Inputs 0 and 4 can be used as up/down counters (32-bit). Inputs 1 and 5 operate as GATES and inputs 2 and 6 control Up/Down.

The EP1518 has three modes of operation that can be selected via the PDOs using the Sync-Manager:

- 2 digital inputs and 2 counters (delivery state)
- 5 digital inputs and 1 counter
- 8 digital inputs

All inputs continue to be shown in the process image even in the counter operation modes. Signal acquisition takes place with the filter times set via CoE.

Irrespective of that, the counter pulses are always counted with a filter of 150 μ s. Further parameters can be set via the CoE objects.

The sensors are supplied via the control voltage US in two groups of four sensors each. Any short circuits on the sensor side are detected and reported to the controller.

The load voltage Up is not used in the input module, but may optionally be connected in order to be relayed downstream.

Quick links

Technical data [> 11] Process image [> 14] Dimensions [> 16] Signal connection [> 23]

3.2 Technical data

All values are typical values over the entire temperature range, unless stated otherwise.

EtherCAT		
Connection	2 x M8 socket, 4-pin, green	
Electrical isolation	500 V	
Distributed Clocks	yes	

Supply voltages		
Connection	Input: M8 connector, 4-pin	
	Downstream connection: M8 socket, 4-pin, black	
U _s nominal voltage	24 V _{DC} (-15 % / +20 %)	
U _s sum current: I _{s,sum}	max. 4 A	
Current consumption from U _s	120 mA + sensor supply	
Rated voltage U _P	24 V _{DC} (-15 % / +20 %)	
U _P sum current: I _{P,sum}	max. 4 A	
Current consumption from U _P	None. U_P is only forwarded.	
Electrical isolation GND_{S} / GND_{P}	no	

Digital inputs		
Number	8	
Connection	4 x M12 socket	
Characteristics	Type 3 according to EN61131-2, compatible with type 1	
Input filter	ajustable: 10 μs … 100 ms	
	Counter inputs: 150 µs	
Signal voltage "0"	-3 +5 V	
Signal voltage "1"	+11 +30 V	
Input current	3 mA	
Sensor supply	from the control voltage U _s , short-circuit proof	
	• max. 0.5 A for channels 0 3 in total	
	• max. 0.5 A for channels 4 7 in total	

Housing data		
Dimensions W x H x D	30 mm x 126 mm x 26.5 mm (without connectors)	
Weight	approx. 165 g	
Installation position	variable	
Material	PA6 (polyamide)	

Environmental conditions		
Ambient temperature during operation	-25 … +60 °C -25 … +55 °C according to cURus	
Ambient temperature during storage	-40 +85 °C	
Vibration resistance, shock resistance	conforms to EN 60068-2-6 / EN 60068-2-27 Additional checks [▶ 13]	
EMC immunity / emission	conforms to EN 61000-6-2 / EN 61000-6-4	
Protection class	IP65, IP66, IP67 (conforms to EN 60529)	

Approvals / markings	
Approvals / markings *)	ATEX, CE, <u>cURus [▶ 24]</u>

*) Real applicable approvals/markings see type plate on the side (product marking).

Additional checks

The boxes have been subjected to the following checks:

Verification	Explanation
Vibration	10 frequency sweeps in 3 axes
	5 Hz < f < 60 Hz displacement 0.35 mm, constant amplitude
	60.1 Hz < f < 500 Hz acceleration 5 g, constant amplitude
Shocks	1000 shocks in each direction, in 3 axes
	35 g, 11 ms

3.3 Scope of supply

Make sure that the following components are included in the scope of delivery:

- 1x EtherCAT Box EP1518-0002
- 2x protective cap for EtherCAT socket, M8, green (pre-assembled)
- 1x protective cap for supply voltage input, M8, transparent (pre-assembled)
- 1x protective cap for supply voltage output, M8, black (pre-assembled)
- 10x labels, blank (1 strip of 10)

Pre-assembled protective caps do not ensure IP67 protection

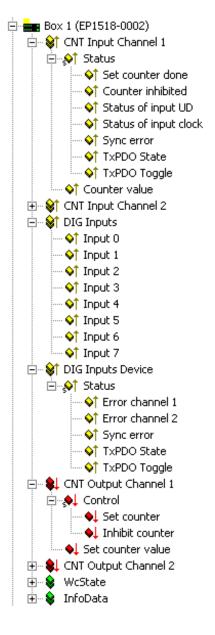
Protective caps are pre-assembled at the factory to protect connectors during transport. They may not be tight enough to ensure IP67 protection.

Ensure that the protective caps are correctly seated to ensure IP67 protection.

3.4 Process image

The process image depends on the selected operation mode [) 36].

Operation mode: 2 counters and 2 digital inputs (delivery state)



The input data of the 1st counter can be found under CNT Input Channel 1.

The adoption of the **Set counter** bit from **CNT Output Channel 1** is displayed with **Set counter done.**

The adoption of the **Inhibit counter** bit from **CNT Output Channel 1** is displayed with **Counter inhibited**.

Status of input UD shows the status of the Up/Down counter input of the 1st counter.

Status of input clock shows the status of the input clock input of the 1st counter.

Sync Error, TxPDO State and TxPDO Toggle are standard EtherCAT process data.

The input data of the 2nd counter can be found under **CNT Input Channel** 2. Their structure corresponds to that of the 1st counter.

DIG Inputs shows the states of the individual inputs irrespective of the selected operation mode.

Error channel 1 displays a short circuit of the supply voltage Us to digital inputs 0 to 3.

Error channel 2 displays a short circuit of the supply voltage Us to digital inputs 4 to 7.

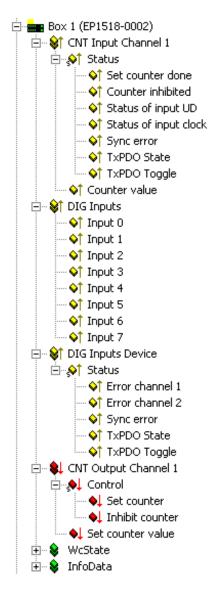
The output data of the 1st counter can be found under **CNT Output Channel 1**.

The setting of **Set counter** activates the adoption of the **Set Counter Value** into the **Counter Value** of the 1st counter. The setting of **Inhibit Counter** disables the 1st counter. Alternatively the counter can be disabled or enabled by the physical GATE input.

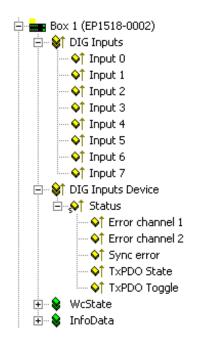
The two values are XORed.

The output data of the 2nd counter can be found under **CNT Output Channel 1**. Their structure corresponds to that of the 1st counter.

Operation mode: 1 counter and 5 digital inputs



Operation mode: 8 digital inputs



The input data of the 1st counter can be found under CNT Input Channel 1.

The adoption of the **Set counter** bit from **CNT Output Channel 1** is displayed with **Set counter done**.

The adoption of the **Inhibit counter** bit from **CNT Output Channel 1** is displayed with **Counter inhibited**.

Status of input UD shows the status of the Up/Down counter input of the 1st counter.

Status of input clock shows the status of the input clock input of the 1st counter.

Sync Error, TxPDO State and TxPDO Toggle are standard EtherCAT process data.

DIG Inputs shows the states of the individual inputs irrespective of the selected operation mode.

Error channel 1 displays a short circuit of the supply voltage Us to digital inputs 0 to 3.

Error channel 2 displays a short circuit of the supply voltage Us to digital inputs 4 to 7.

The output data of the 1st counter can be found under **CNT Output Channel 1**.

The setting of **Set counter** activates the adoption of the **Set Counter Value** into the **Counter Value** of the 1st counter. The setting of **Inhibit Counter** disables the 1st counter. Alternatively the counter can be disabled or enabled by the physical GATE input.

The two values are XORed.

DIG Inputs shows the states of the individual inputs irrespective of the selected mode.

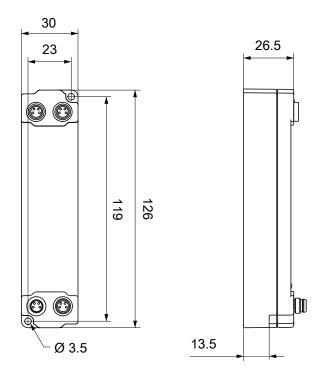
Error channel 1 displays a short circuit of the supply voltage Us to digital inputs 0 to 3.

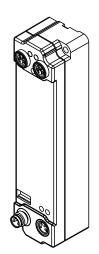
Error channel 2 displays a short circuit of the supply voltage Us to digital inputs 4 to 7.

4 Installation

4.1 Mounting

4.1.1 Dimensions





All dimensions are given in millimeters. The drawing is not true to scale.

Housing features

Housing material	PA6 (polyamide)
Sealing compound	polyurethane
Mounting	two mounting holes Ø 3.5 mm for M3
Metal parts	brass, nickel-plated
Contacts	CuZn, gold-plated
Power feed through	max. 4 A
Installation position	variable
Protection class	IP65, IP66, IP67 (conforms to EN 60529) when screwed together
Dimensions (H x W x D)	approx. 126 x 30 x 26.5 mm (without connectors)

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4.1.2 Fixing

NOTE

Dirt during assembly

Dirty connectors can lead to malfunctions. Protection class IP67 can only be guaranteed if all cables and connectors are connected.

• Protect the plug connectors against dirt during the assembly.

Mount the module with two M3 screws on the mounting holes in the corners of the module. The mounting holes have no thread.

4.1.3 Tightening torques for plug connectors

Screw connectors tight with a torque wrench. (e.g. ZB8801 from Beckhoff)

Connector diameter	Tightening torque
M8	0.4 Nm
M12	0.6 Nm

4.2 Connection

4.2.1 EtherCAT

4.2.1.1 Connectors

NOTE

Risk of confusion: supply voltages and EtherCAT

Defect possible through incorrect insertion.

 Observe the color coding of the connectors: black: Supply voltages green: EtherCAT

EtherCAT Box Modules have two green M8 sockets for the incoming and downstream EtherCAT connections.



Connection



Fig. 4: M8 socket

EtherCAT	M8 connector	Core colors		
Signal	Contact	ZB9010, ZB9020, ZB9030, ZB9032, ZK1090-6292, ZK1090-3xxx-xxxx	9032, ZB9031 and old versions of ZB9030, ZB9032, ZK1090-3xxx- xxxx	
Tx +	1	yellow ¹⁾	orange/white	white/orange
Tx -	4	orange ¹⁾	orange	orange
Rx +	2	white ¹⁾	blue/white	white/green
Rx -	3	blue ¹⁾	blue	green
Shield	Housing	Shield	Shield	Shield

¹⁾ Core colors according to EN 61918

Adaptation of core colors for cables ZB9030, ZB9032 and ZK1090-3xxxx-xxxx

For standardization, the core colors of the ZB9030, ZB9032 and ZK1090-3xxx-xxxx cables have been changed to the EN61918 core colors: yellow, orange, white, blue. So there are different color codes in circulation. The electrical properties of the cables have been retained when the core colors were changed.

4.2.1.2 Status LEDs



L/A (Link/Act)

A green LED labelled "L/A" is located next to each EtherCAT socket. The LED indicates the communication state of the respective socket:

LED	Meaning
off	no connection to the connected EtherCAT device
lit	LINK: connection to the connected EtherCAT device
flashes	ACT: communication with the connected EtherCAT device

Run

Each EtherCAT slave has a green LED labelled "Run". The LED signals the status of the slave in the EtherCAT network:

LED	Meaning
off	Slave is in "Init" state
flashes uniformly	Slave is in "Pre-Operational" state
flashes sporadically	Slave is in "Safe-Operational" state
lit	Slave is in "Operational" state

Description of the EtherCAT slave states

4.2.1.3 Cables

For connecting EtherCAT devices only shielded Ethernet cables that meet the requirements of at least category 5 (CAT5) according to EN 50173 or ISO/IEC 11801 should be used.

EtherCAT uses four wires for signal transmission.

Thanks to automatic line detection ("Auto MDI-X"), both symmetrical (1:1) or cross-over cables can be used between Beckhoff EtherCAT.

Detailed recommendations for the cabling of EtherCAT devices

4.2.2 Supply voltages

A WARNING

Power supply from SELV/PELV power supply unit!

SELV/PELV circuits (Safety Extra Low Voltage, Protective Extra Low Voltage) according to IEC 61010-2-201 must be used to supply this device.

Notes:

- SELV/PELV circuits may give rise to further requirements from standards such as IEC 60204-1 et al, for example with regard to cable spacing and insulation.
- A SELV (Safety Extra Low Voltage) supply provides safe electrical isolation and limitation of the voltage without a connection to the protective conductor, a PELV (Protective Extra Low Voltage) supply also requires a safe connection to the protective conductor

Observe the UL requirements

• When operating under UL conditions, observe the warnings in the chapter UL Requirements [> 24].

The EtherCAT Box has one input for two supply voltages:

• Control voltage U_s

The following sub-functions are supplied from the control voltage U_s:

- the fieldbus
- the processor logic
- typically the inputs and the sensors if the EtherCAT Box has inputs.
- Peripheral voltage U_P

For EtherCAT Box modules with digital outputs the digital outputs are typically supplied from the peripheral voltage U_P . U_P can be supplied separately. If U_P is switched off, the fieldbus function, the function of the inputs and the supply of the sensors are maintained.

The exact assignment of U_s and U_P can be found in the pin assignment of the I/O connections.

Redirection of the supply voltages

The power IN and OUT connections are bridged in the module. Hence, the supply voltages U_s and U_p can be passed from EtherCAT Box to EtherCAT Box in a simple manner.

NOTE

Note the maximum current!

Ensure that the permitted current for the connectors is not exceeded when routing the supply voltages U_s and U_p :

M8 connector: max. 4 A 7/8" connector: max 16 A

NOTE

Unintentional cancellation of the electrical isolation of GND_s and GND_P possible.

In some types of EtherCAT Box modules the ground potentials GND_s and GND_P are connected.

• If several EtherCAT Box modules are supplied with the same electrically isolated voltages, check whether there is an EtherCAT Box among them in which the ground potentials are connected.

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4.2.2.1 Connectors

NOTE

Risk of confusion: supply voltages and EtherCAT

Defect possible through incorrect insertion.

 Observe the color coding of the connectors: black: Supply voltages green: EtherCAT

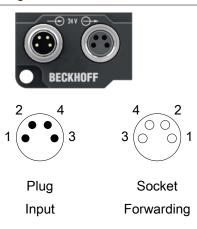


Fig. 5: M8 connector

Contact	Function	Description	Core color ¹⁾
1	Us	Control voltage	Brown
2	U _P	Peripheral voltage	White
3	GND	GND	Blue
4	GND	GND	Black

¹⁾ The core colors apply to cables of the type: Beckhoff ZK2020-3xxx-xxxx

4.2.2.2 Status LEDs

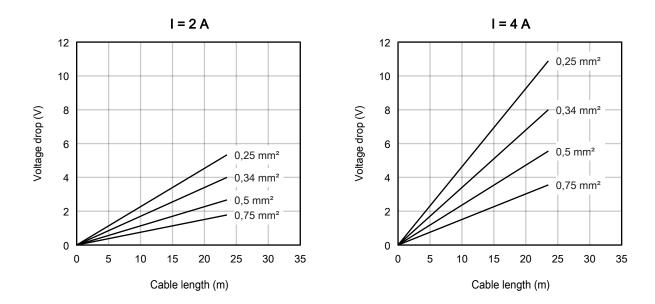


LED	Display	Meaning
U _s (control voltage)	off	The supply voltage U _s is not available.
	green illuminated	The supply voltage U _s is available.
U _P (peripheral voltage)	off	The supply voltage U_P is not available.
	green illuminated	The supply voltage U_P is available.

4.2.2.3 Conductor losses

Take into account the voltage drop on the supply line when planning a system. Avoid the voltage drop being so high that the supply voltage at the box lies below the minimum nominal voltage. Variations in the voltage of the power supply unit must also be taken into account.

Voltage drop on the supply line



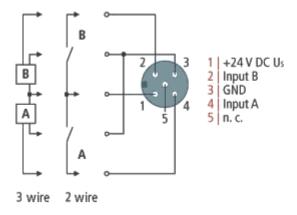
4.2.3 Signal connection

Pin assignment

M12 socket	Pin	Symbol	Function	
	1	+24 V Us	Sensor supply	
	2	Input B	Digital input	
	3	GND	Ground	
	4	Input A	Digital input	
	5	n.c.	-	

Use the sensor supply at pin 1 as supply voltage for connected sensors. Specifications [> 11].

Connection examples



Status LEDs

Each M12 socket has two green LEDs. An LED lights up when a high level is detected at the respective input.



Input A Input B

4.3 UL Requirements

The installation of the EtherCAT Box Modules certified by UL has to meet the following requirements.

Supply voltage

CAUTION!

This UL requirements are valid for all supply voltages of all marked EtherCAT Box Modules! For the compliance of the UL requirements the EtherCAT Box Modules should only be supplied

- by a 24 V_{DC} supply voltage, supplied by an isolating source and protected by means of a fuse (in accordance with UL248), rated maximum 4 Amp, or
- by a 24 V_{DC} power source, that has to satisfy NEC class 2.
 A NEC class 2 power supply shall not be connected in series or parallel with another (class 2) power source!

▲ CAUTION

CAUTION!

To meet the UL requirements, the EtherCAT Box Modules must not be connected to unlimited power sources!

Networks

CAUTION!

To meet the UL requirements, EtherCAT Box Modules must not be connected to telecommunication networks!

Ambient temperature range

▲ CAUTION

CAUTION!

To meet the UL requirements, EtherCAT Box Modules has to be operated only at an ambient temperature range of -25 °C to +55 °C!

Marking for UL

All EtherCAT Box Modules certified by UL (Underwriters Laboratories) are marked with the following label.



Fig. 6: UL label

4.4 ATEX notes

4.4.1 ATEX - Special conditions

WARNING

Observe the special conditions for the intended use of EtherCAT Box modules in potentially explosive areas – directive 94/9/EU.

- The certified components are to be installed with a <u>BG2000-0000 or BG2000-0010 protection enclosure</u> [<u>> 26]</u> that guarantees a protection against mechanical hazards!
- If the temperatures during rated operation are higher than 70°C at the feed-in points of cables, lines or pipes, or higher than 80°C at the wire branching points, then cables must be selected whose temperature data correspond to the actual measured temperature values!
- Observe the permissible ambient temperature range of 0 to 55°C for the use of EtherCAT Box modules in potentially explosive areas!
- Measures must be taken to protect against the rated operating voltage being exceeded by more than 40% due to short-term interference voltages!
- The connections of the certified components may only be connected or disconnected if the supply voltage has been switched off or if a non-explosive atmosphere is ensured!

Standards

The fundamental health and safety requirements are fulfilled by compliance with the following standards:

- EN 60079-0: 2006
- EN 60079-15: 2005

Marking

The EtherCAT Box modules certified for potentially explosive areas bear the following marking:

(x) II 3 G Ex nA II T4 DEKRA 11ATEX0080 X Ta: 0 - 55°C

or



Batch number (D number)

The EtherCAT Box modules bear a batch number (D number) that is structured as follows:

D: WW YY FF HH

WW - week of production (calendar week) YY - year of production FF - firmware version HH - hardware version

Example with batch number 29 10 02 01:

29 - week of production 29

- 10 year of production 2010
- 02 firmware version 02
- 01 hardware version 01

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4.4.2 BG2000 - EtherCAT Box protection enclosures

Risk of electric shock and damage of device!

Bring the EtherCAT system into a safe, powered down state before starting installation, disassembly or wiring of the modules!

ATEX

Mount a protection enclosure!

To fulfill the <u>special conditions according to ATEX [▶ 25]</u>, a BG2000-0000 or BG2000-0010 protection enclosure has to be mounted over the EtherCAT Box.

Installation

Put the cables for EtherCAT, power supply and sensors/actuators through the hole of the protection enclosure.

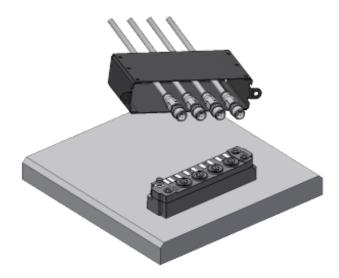


Fig. 7: BG2000 - putting the cables

Fix the wires for EtherCAT, power supply and sensors/actuators to the EtherCAT Box.

BECKHOFF

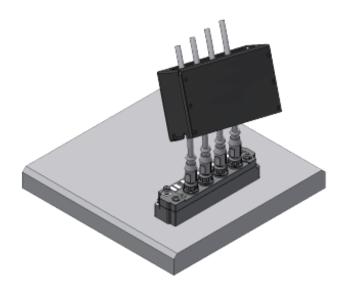


Fig. 8: BG2000 - fixing the cables

Mount the protection enclosure over the EtherCAT Box.

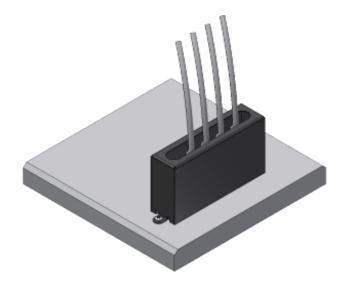


Fig. 9: BG2000 - mounting the protection enclosure

4.4.3 ATEX Documentation

Notes about operation of EtherCAT Box Modules (EPxxxx-xxxx) in potentially explosive areas (ATEX)

Pay also attention to the continuative documentationNotes about operation of EtherCAT Box Modules (EPxxxx-xxxx) in potentially explosive areas (ATEX) that is available in the download area of the Beckhoff homepage http://www.beckhoff.com!





Disposal



Products marked with a crossed-out wheeled bin shall not be discarded with the normal waste stream. The device is considered as waste electrical and electronic equipment. The national regulations for the disposal of waste electrical and electronic equipment must be observed.

5 Commissioning and configuration

5.1 Integrating into a TwinCAT project

The procedure for integration in a TwinCAT project is described in these <u>Quick start guide</u>.

5.2 Distributed Clocks (DC)

• EtherCAT System Documentation

A basic introduction to the topic of EtherCAT and Distributed Clocks is available in the <u>Download</u> area on the Beckhoff homepage: <u>EtherCAT System Documentation</u>.

The Box supports the distributed clocks functionality. In order for the Box to provide the current counter reading before the arrival of the querying EtherCAT datagram in the specified process data, a corresponding signal must be generated cyclically in the Box. This signal can be triggered in the Box by two events: the SyncManager (SM) and the distributed clock (DC). Under operation mode selection the following options are available (see Fig. "DC" (Disributed Clocks) tab)

SM-synchron

The SyncManager event occurs when an EtherCAT frame successfully exchanges process data with the Box. Frame-triggered, the current counter value is thus cyclically determined, but with the low temporal jitter of the Ethernet frame.

• DC-synchron

In DC operation mode determination of the counter value is triggered cyclically at constant intervals through the integrated DC unit, synchronous with the bus cycle as standard. More uniform polling offers higher-quality position data for a higher-level control algorithm, for example. The trigger for the counter reading is the SYNC0 signal.

General EtherCAT DC	Process data Online
Operation Mode:	Synchron
	Advanced Settings
	•

Fig. 10: DC tab (Distributed Clocks)

When *DC-Synchron* operation mode is activated TwinCAT selects settings that ensure reliable operation of the Box with current position data. This means that determination of the current counter value is triggered by the SYNC0 signal at highly constant intervals and in good time (i.e. with an adequate safety buffer) before retrieving EtherCAT datagram is started.

If necessary, the SYNC0 signal can be shifted along the time axis to the right/later or left/earlier in associated dialogs by specifying a user-defined shift time, see Fig. *Advanced Distributed Clock (DC) settings*.

- A right-shift (positive shift value) will delay the counter value query, which means the position value becomes more current from the PLC perspective. However, this increases the risk that the position determination may not be finished in time before the arrival of EtherCAT frame, so that no current position value is available in this cycle.
- A left-shift (negative shift value) means the counter value will be queried earlier, resulting in older position values, with an associated increase in the safety buffer before the arrival of the EtherCAT datagram. This setting may be useful in systems with high real-time jitter, if no Industrial PCs from Beckhoff are used for control purposes, for example.

NOTE

Risk of device damage

The mentioned notes and information should be used advisedly.

The EtherCAT master automatically allocates SYNC0 and SYNC1 settings that support reliable and timely process data acquisition.

User intervention at this point may lead to undesired behavior.

If these settings are changed in the System Manager, no plausibility checks are carried out on the software side. Correct function cannot be guaranteed for all conceivable setting options.

Default setting

The cyclic reading of the inputs is triggered by the SYNC0 pulse (interrupt) of the DC in the EtherCAT Box. The EtherCAT master sets the *Sync Unit Cycle* time value to the PLC cycle time and therefore the EtherCAT cycle time as standard. See Fig. *Advanced Distributed Clock (DC) settings*: 4000 μ s = 4 ms, as TwinCAT is in configuration mode.

DC settings

Advanced Settings	×
Advanced Settings	Distributed Clock Cyclic Mode Operation Mode: SM-Synchron Enable Sync Unit Cycle (µs): Sync Unit Cycle (µs): Sync Unit Cycle (µs): User Defined SYNC 0 Enable SYNC 0 SYNC 0 SYNC 0 SYNC 0 SYNC 1 Sync Unit Cycle (µs): Cycle Time (µs): (µs) SYNC 0 SYNC 0
	 SYNC 0 Cycle x 1 Shift Time (μs): Enable SYNC 1 Use as potential Reference Clock OK Cancel

Fig. 11: Advanced settings for Distributed Clocks (DC)

SYNC0

Sync Unit cycle: a multiple of the bus cycle time. The counter value is periodically determined at this interval (in μ s).

User defined

Any number up to 2³² ns, or about 4.3 seconds. Use of decimal points is allowed.

Shift Time

The Shift Time can be used to shift the SYNC0 pulse for this EtherCAT Box relative to other EtherCAT devices and the global SYNC pulse in nanosecond steps. If the inputs of several Boxes are read simultaneously, the same value must be entered here.

Based on input reference

If this option is activated an additional Input Shift is added to the configurable terminal-specific SYNC0 shift (user defined). This value is calculated and made available by the EtherCAT master (SysMan/Device EtherCAT/Tab EtherCAT/Advanced Settings/Distributed Clocks/Input Shift Time). As a result, *all* input terminals in the system (EL1xxx, EL3xxx, EP1xxx, EP3xxx, EPP1xxx, EPP3xxx) read in their inputs as shortly as possible before the EtherCAT frame is fetched, so that controller can be supplied with input data that is as current as possible.

Enable SYNC0

Automatically activated in DC-synchron operation mode.

SYNC1

Additional SYNC pulse, derived from SYNC0 or from the DC itself.

DC settings for EtherCAT master

Higher-level distributed clock parameters can be modified under advanced settings for the EtherCAT master. Refer also to the basic introduction to the topic of EtherCAT and Distributed Clocks; download: the Distributed Clocks system description.



Fig. 12: EtherCAT Master, EtherCAT tab, Advanced Settings

5.3 Distributed Clocks and EP1518

Digital inputs and Distributed Clocks

If the Distributed Clock of the EP1518 is activated, the digital inputs are read in without the set filter promptly before the arrival of the querying EtherCAT datagram.

5.4 Configuration

5.4.1 Basic Function Principles

The EP1518-0002 has 8 digital inputs. Of these, inputs 0, 1 and 2 as well as 4, 5 and 6 can each be used for one counter. The states of the individual inputs are always illustrated in the process image, irrespective of their use.

Input	M12	Properties
0	Socket 1, Pin 4	Digital input or counter input 1
1	Socket 1, Pin 2	Digital input or Gate 1
2	Socket 2, Pin 4	Digital input or Up/Down 1
3	Socket 2, Pin 2	Digital input
4	Socket 3, Pin 4	Digital input or counter input 2
5	Socket 3, Pin 2	Digital input or Gate 2
6	Socket 4, Pin 4	Digital input or Up/Down 2
7	Socket 4, Pin 2	Digital input

Operation modes

Three <u>operation modes [} 36]</u> are available. The setting takes place by selecting the PDOs in the Sync-Manager:

Operation mode	Number of counter channels	Number of "free" digital inputs	Properties
2 up/down counters (32- bit)	2	2	Single pulses are counted at the counter inputs.
2 up/down counters (32- bit)	1	5	The gate input or the software gate enables the counter. The count direction is specified via CoE.
8 digital inputs, no counter	-	8	digital inputs: Filter for inputs 0 and 4 permanently set to 150 μs. The filter setting for the other inputs is configurable by software.

The GATE and Up/Down inputs can be converted to standard inputs.

Counting mode

The following settings for GATE and Up/Down can be combined and apply independently to each counter.

Counting mode with standard setting (up counter)

In the delivery state the Counter Value is incremented on each rising edge. The count direction is up.

The counter is disabled by applying a high level to the GATE input or by setting the Inhibit Counters bit.

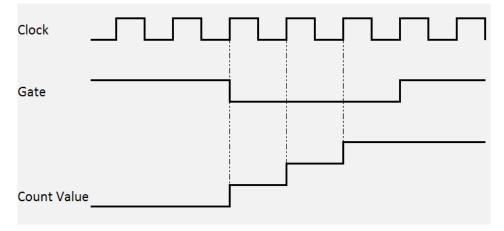


Fig. 13: Counting mode with standard setting

Counting mode with reversed count direction (down counter)

The count direction is changed by applying a high level to the Up/Down input or by setting the CoE object 0x80x0:04 *Count down.* The count direction is down.

Clock					
Gate					
Up/Down					
Count Valu	e		1		

Fig. 14: Counting mode with reversed counting direction

Counting mode with inverted (negated) GATE input

In the default setting the counter is disabled by applying a high level to the GATE input or by setting the *Inhibit Counters* bit

Setting the CoE object 0x80x0:05 *Enable input gate* activates the counter if GATE is set and deactivates it if GATE is not set.

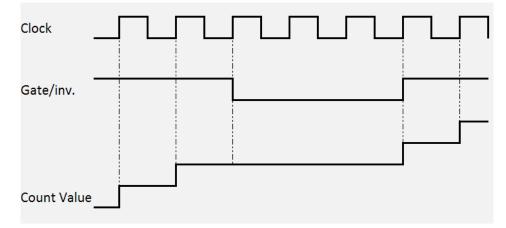


Fig. 15: Counting mode with inverted GATE input

I	Index	Name		Flags	Value			
I	Ė~ 8000:0	CNT Settings		RW	>19<			
I	8000:03	Enable reload		RW	TRUE			
I	8000:04	Count down		RW	FALSE			
I	8000:05	Operating mode		RW	Enable pos. gate (Gat	e sperrt mit		
I	8000:07	Enable input gate		BW	FALSE			
l	8000:08 8000:13	Enable input UD Counter reload value	Set Value Dialo	og		ĺ	×	
I	± 8010:0	CNT Settings	Dec:	2			1 L -	
I		DIG Filter Settings	Dec:	2		OK	1	
I		_	Hex:	0x00000002		Cancel		
I			Enum:	Enable neg. gate (Gate sperrt mit negat 💌				
I					gate (Gate sperrt mit pos gate (Gate sperrt mit neo			
l			Bool:	0	1	Hex Edit	L	
I			Binary:	02 00 00 00		4		
l			Bit Size:	O 1 O 8	○ 16	0?		

Fig. 16: CoE for inverting the GATE input

5.4.2 Operation modes

Operation mode selection

There are three operation modes to choose from:

- <u>Two up/down counters [b_37]</u>(delivery state)
- One up/down counter [• 38]
- <u>8 digital inputs [• 39]</u>

The setting takes place by selecting the PDOs in the Sync-Manager:

Operation mode	Number of 32-bit coun- ters	Number of "free" digital inputs	Properties	
2 up/down counters	2	2	Single pulses at the counter inputs are	
1 up/down counters	1	5	counted; the Gate input or the software gate enables the counter; count direction via CoE	
8 digital inputs, no counter	-	8	digital inputs: Filter for inputs 0 and 4 permanently set to 150 µs. The filter setting for the other inputs is configurable by software.	

Operation mode set- tings of the PDOs	0x1600	0x1601	0x1A00	0x1A01	0x1A02	0x1A03	Comments	
2 up/down counters (32-bit)	1	1	1	1	0/1	0/1	2 counters, digital inputs, diagnosis of Us	
1 up/down counters (32-bit)	1	0	1	0	0/1	0/1		
8 digital inputs, no counter	0	0	0	0	0/1	0/1	digital inputs: Filter for inputs 0 and 4 permanently set to 150 µs. The filter setting for the other inputs is configurable by software.	

The module parameters are set in the CoE objects 0x8000:0 for counter 1, 0x8010:0 for counter 2 and 0x8022:0 for the digital inputs. (integrate links)



The second counter is always active internally

Internally the second counter is always active, so that when switching from one 32-bit counter to two 32-bit counters the previous incoming pulses were counted in the second counter and saved in the Counter Value.

Two up/down counters

This is the operation mode in the factory setting.

General EtherCAT DC Process	Data Startup CoE - Online C)nline	
Sync Manager:	PDO List:		
SM Size Type Flags 0 256 MbxOut 1 1 256 MbxIn 2 12 Outputs 3 16 Inputs	0x1A01 6.0 CNT I 0x1A02 2.0 DIG Ir	nput Channel 1 F nput Channel 2 F	SM SU 3 0 3 0 3 0 3 0 3 0 3 0
DDQ Assistant (0:1012)	0x1600 6.0 CNT (Dutput Channel 1 F Dutput Channel 2 F	2 0 2 0
PD0 Assignment (0x1C12): ♥ 0x1600 ♥ 0x1601	Index Size Offs ··· 0.2 0.0 0x6000:03 0.1 0.2 0x6000:05 0.1 0.3 0x6000:05 0.1 0.4 0x6000:06 0.1 0.5 ··· 0.7 0.6 0x6000:0F 0.1 1.5 0x6000:0F 0.1 1.6 0x6000:0F 0.1 1.7 0x6000:10 0.1 1.7 0x6000:11 4.0 2.0	Name Status_Set counter done Status_Counter inhibited Status_Status of input UD Status_Status of input clock Status_Sync error Status_TxPD0 State Status_TxPD0 Toggle Counter value	Type Default (hex) BOOL BOOL BOOL BOOL BOOL BOOL BOOL UDINT
Download I PD0 Assignment I PD0 Configuration General EtherCAT DC Process Sync Manager:	Predefined PDO Assignment: (Load PDO info from device Sync Unit Assignment Data Startup CoE - Online C PDO List:		
Syne manager. SM Size Type Flags 0 256 MbxDut 1 256 MbxIn 2 12 Outputs 3 16 Inputs	Index Size Name 0x1A00 6.0 CNT I 0x1A01 6.0 CNT I 0x1A02 2.0 DIG Ir 0x1A03 2.0 DIG Ir 0x1600 6.0 CNT 0	nput Channel 1 F nput Channel 2 F	SM SU 3 0 3 0 3 0 3 0 2 0 2 0 2 0
PD0 Assignment (0x1C12): ♥ 0x1A00 ♥ 0x1A01 ♥ 0x1A02 ♥ 0x1A03	Index Size Offs ··· 0.2 0.0 0x6000:03 0.1 0.2 0x6000:04 0.1 0.3 0x6000:05 0.1 0.4 0x6000:05 0.1 0.4 0x6000:06 0.1 0.5 ··· 0.7 0.6 0x6000:0F 0.1 1.5 0x6000:0F 0.1 1.6 0x6000:0F 0.1 1.7 0x6000:10 0.1 1.7 0x6000:11 4.0 2.0	Name Status_Set counter done Status_Counter inhibited Status_Status of input UD Status_Status of input clock Status_Sync error Status_TxPD0 State Status_TxPD0 Toggle Counter value	Type Default (hex) BOOL BOOL BOOL BOOL BOOL BOOL BOOL UDINT
Download	Predefined PDO Assignment: (none)	

The PDOs $0x1600 [\blacktriangleright 49]$, $0x1601 [\blacktriangleright 50]$ as well as $0x1A00 [\blacktriangleright 50]$, $0x1A01 [\blacktriangleright 50]$, $0x1A02 [\blacktriangleright 51]$ and $0x1A03 [\blacktriangleright 51]$ are activated. The meaning of the individual objects is explained in the object description.

One up/down counter

This mode can be set as follows:

General EtherCAT DC Process	Data Startup CoE - Online Online	
Sync Manager:	PDO List:	
SMSizeTypeFlags0256MbxOut1256MbxIn26Outputs310Inputs	Index Size Name Flags SM SU 0x1A00 6.0 CNT Input Channel 1 F 3 0 0x1A01 6.0 CNT Input Channel 2 F 0 0x1A02 2.0 DIG Inputs F 3 0 0x1A03 2.0 DIG Inputs Device F 3 0	
BD0 Assistment (0:1012)	0x1600 6.0 CNT Output Channel 1 F 2 0 0x1601 6.0 CNT Output Channel 2 F 0 PD0 Content (0x1A00): Content (0x1A00): Content (0x1A00): Content (0x1A00): Content (0x1A00):	
PD0 Assignment (0x1C12): ♥ 0x1600 □ 0x1601	Index Size Offs Name Type Default (hex) 0.2 0.0 0x6000:03 0.1 0.2 Status_Set counter done BOOL 0x6000:04 0.1 0.3 Status_Counter inhibited BOOL 0x6000:05 0.1 0.4 Status_Status of input UD BOOL 0x6000:05 0.1 0.4 Status_Status of input UD BOOL 0x6000:06 0.1 0.5 Status_Status of input clock BOOL 0x6000:06 0.1 0.5 Status_Status of input clock BOOL 0x6000:06 0.1 1.5 Status_Sync error BOOL 0x6000:0F 0.1 1.6 Status_TxPD0 State BOOL 0x600L 0x600L	
	Predefined PDO Assignment: (none) Load PDO info from device Sync Unit Assignment Data Startup CoE - Online Online PDO List:	
Sync Manager: SM Size Type Flags 0 256 MbxOut 1 256 MbxIn 2 6 Outputs 3 10 Inputs	Index Size Name Flags SM SU 0x1A00 6.0 CNT Input Channel 1 F 3 0 0x1A01 6.0 CNT Input Channel 2 F 0 0x1A02 2.0 DIG Inputs F 3 0 0x1A03 2.0 DIG Inputs Device F 3 0 0x1600 6.0 CNT Output Channel 1 F 2 0 0x1601 6.0 CNT Output Channel 2 F 0 0	
PD0 Assignment (0x1C12): ♥ 0x1A00 ○ 0x1A01 ♥ 0x1A02 ♥ 0x1A03	Index Size Offs Name Type Default (hex) 0.2 0.0 0.2 0.0 0x6000:03 0.1 0.2 Status_Set counter done BOOL 0x6000:04 0.1 0.3 Status_Counter inhibited BOOL 0x6000:05 0.1 0.4 Status_Status of input UD BOOL 0x6000:06 0.1 0.5 Status_Status of input clock BOOL 0x6000:06 0.1 1.5 Status_Status of input clock BOOL 0x6000:06 0.1 1.5 Status_Status of input clock BOOL 0x6000:06 0.1 1.5 Status_TxPDO State BOOL 0x6000:07 0.1 1.6 Status_TxPDO State BOOL	•
Download PD0 Assignment PD0 Configuration	Predefined PD0 Assignment: (none) Load PD0 info from device Sync Unit Assignment	V

The PDOs 0x1600 [\blacktriangleright 49] as well as 0x1A00 [\blacktriangleright 50], 0x1A02 [\blacktriangleright 51] and 0x1A03 [\blacktriangleright 51] are activated. The CoE objects are identical to the 2 x 32-bit counter operation mode.

8 digital inputs, no counter

This mode can be set as follows:

General EtherCAT DC Process Data Startup CoE - Online Online							
Sync Manager:	PDO List:						
SM Size Type Flags	Index	Size	Name	Flags	SM	SU	
0 256 MbxOut	0x1A00	6.0	CNT Input Channel 1	F		0	
1 256 MbxIn	0x1A01	6.0	CNT Input Channel 2	F		0	
2 0 Outputs	0x1A02	2.0	DIG Inputs	F	3	0	
3 4 Inputs	0x1A03	2.0	DIG Inputs Device	F	3	0	
-	0x1600	6.0	CNT Output Channel 1	F		0	
	0x1601	6.0	CNT Output Channel 2	F		0	
PD0 Assignment (0x1C12):	PDO Content						
0x1A00	Index	Size	Offs Name		Туре	Default (hex)	
🗆 0x1A01		0.2	0.0				
✓ 0x1A02	0x6000:03	0.1	0.2 Status_Set cour		BOOL		
✓ 0x1A03	0x6000:04	0.1	0.3 Status_Counter		BOOL		
	0x6000:05	0.1	0.4 Status_Status of		BOOL		
	0x6000:06	0.1	0.5 Status_Status of	f input clock	BOOL		
		0.7	0.6				
	0x6000:0E	0.1	1.5 Status_Sync err		BOOL		
	0x6000:0F	0.1	1.6 Status_TxPDO Status_TxPDO Status_TxPDO Status		BOOL		
	0x6000:10		1.7 Status_TxPD0	l oggle	BOOL		
	0x6000:11	4.0	2.0 Counter value		UDINT		
			6.0				
Download	Predefined F	PDO Assia	nment: (none)				-
PD0 Assignment	1	_					
	Load PDO in	nfo from de	evice				
PDO Configuration	Sync Unit A:	ssignment.					

The PDOs 0x1A02 [\blacktriangleright 51] and 0x1A03 [\blacktriangleright 51] are activated. The meaning of the individual objects is explained in the object description.

5.4.3 Counter settings

Enabling the GATE and Up/Down inputs as standard inputs

By setting the CoE objects *Enable Input gate* and *Enable input UD*, the inputs are no longer allocated to the counters, but are used as standard inputs.

Index	Name	Flags	Value
Ė~ 8000:0	CNT Settings	R₩	> 19 <
8000:03	Enable reload	RW	TRUE
8000:04	Count down	RW	FALSE
8000:05	Operating mode	BW	Enable pos. gate (Gate sperrt mit
8000:07	Enable input gate	RW	FALSE
8000:08	Enable input UD	RW	FALSE
8000:13	Counter reload value	R₩	0x0000C350 (50000)
主 🛛 8010:0	CNT Settings	RW	> 19 <
· ± ··· 8022:0	DIG Filter Settings	RW	>8<

Fig. 17: Enabling the inputs

Setting the counter to a value specified by the process data

The counter (counter value) can be set to any desired value by the controller.

To do this, set the desired value in *Set counter value*. The value is then adopted by *Counter value* on a rising edge of the *Set counter* control bit.

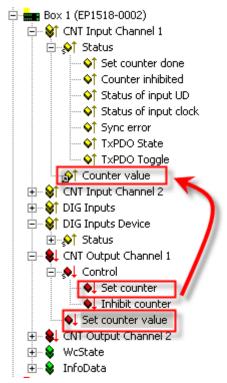


Fig. 18: Setting the counter

Automatic setting/resetting of the counter to a defined value

If a desired value is set in *Counter reload value* and the *Enable reload* bit is activated, the counter is set to 0 or to the set value if the specified value is exceeded or fallen below (depending on the counting direction).

BECKHOFF

Index	Name	Flags	Value
É~ 8000:0	CNT Settings	RW	> 19 <
8000:03	Enable reload	BW	TRUE
8000:04	Count down	BW	FALSE
8000:05	Operating mode	RW	Enable pos. gate (Gate sperrt mit
8000:07	Enable input gate	RW	FALSE
8000:08	Enable input UD	RW	FALSE
8000:13	Counter reload value	R₩	0x0000C350 (50000)
± 8010:0	CNT Settings	RW	> 19 <
± 8022:0	DIG Filter Settings	RW	>8<
		Set Value Dia	alog 🔰 🚺
		Dec:	БОООО ОК
		Hex:	0x0000C350 Cancel
		Float:	50000
		Bool:	0 1 Hex Edit
		Binary:	50 C3 00 00 4
		Bit Size:	○1 ○8 ○16 ●32 ○64 ○ ?

Fig. 19: Automatic setting of the counter

5.4.4 Restoring the delivery state

To restore the delivery state for backup objects in ELxxxx terminals / EPxxxx- and EPPxxxx box modules, the CoE object *Restore default parameters, SubIndex 001* can be selected in the TwinCAT System Manager (Config mode).

	Update Li	st 🗌 Auto Update 🗹 Sin	gle Update	Show Offline Data		
_	Advanced			-		
	Add to Start	up	Module OD (A	AoE Port): 0		
Ind	lex	Name	Flags	Value	Unit	^
	1000	Device type	RO	0x00001389 (5001)		
	1008	Device name	RO	EL5101		
	1009	Hardware version	RO			
	100A	Software version	RO			
-	1011:0	Restore default parameters	RO	>1<		
	- 1011:01	SubIndex 001	RW	0x0000000 (0)		
÷	1018:0	Identity	RO	> 4 <		
+	10F0:0	Backup parameter handling	RO	>1<		
+	1400:0	RxPDO-Par Outputs	RO	> 6 <		
÷	1401:0	RxPDO-Par Outputs Word-Aligned	RO	> 6 <		
+	1402:0	ENC RxPDO-Par Control compact	RO	> 6 <		
+	1403:0	ENC RxPDO-Par Control	RO	> 6 <		
+	1600:0	RxPDO-Map Outputs	RO	>2<		
+	1601:0	RxPDO-Map Outputs Word-Aligned	RO	>3<		
÷	1602:0	ENC BxPDO-Map Control compact	RO	>7<		~

Fig. 20: Selecting the Restore default parameters PDO

Double-click on *SubIndex 001* to enter the Set Value dialog. Enter the value **1684107116** in field *Dec* or the value **0x64616F6C** in field *Hex* and confirm with OK.

All backup objects are reset to the delivery state.

Set Value Dia	alog ×
Dec:	1684107116 OK
Hex:	0x64616F6C Cancel
Float:	1.6634185e+22
Bool:	<u>0</u> <u>1</u> Hex Edit
Binary:	6C 6F 61 64 4
Bit Size:	○1 ○8 ○16 ●32 ○64 ○?

Fig. 21: Entering a restore value in the Set Value dialog

Alternative restore value In some older terminals / boxes the backup objects can be switched with an alternative restore value: Decimal value: 1819238756 Hexadecimal value: 0x6C6F6164 An incorrect entry for the restore value has no effect.

5.5 CoE objects

5.5.1 Object overview

EtherCAT XML Device Description

The display matches that of the CoE objects from the EtherCAT <u>XML</u> Device Description. We recommend downloading the latest XML file from the download area of the Beckhoff website and installing it according to installation instructions.

Index (h	ex)	Name	Flags	Default value
1000 [▶ 4	19]	Device type	RO	0x00001389 (5001 _{dec})
1008 [▶ 4	19]	Device name	RO	EP1518-0002
1009 [▶ 4	19]	Hardware version	RO	00
100A [)_4	19]	Software version	RO	01.03
1011:0	Subindex	Restore default parameters	RO	0x01 (1 _{dec})
[▶_47]	1011:01	SubIndex 001	RW	0x0000000 (0 _{dec})
1018:0	Subindex	Identity	RO	0x04 (4 _{dec})
[▶_49]	1018:01	Vendor ID	RO	0x0000002 (2 _{dec})
	1018:02	Product code	RO	0x05EE4052 (99500114 _{dec})
	1018:03	Revision	RO	0x00100002 (1048578 _{dec})
	1018:04	Serial number	RO	0x0000000 (0 _{dec})
10F0:0	Subindex	Backup parameter handling	RO	0x01 (1 _{dec})
[▶_49]	10F0:01	Checksum	RO	0x0000000 (0 _{dec})
1600:0	Subindex	CNT RxPDO-Map Outputs Ch.1	RO	0x05 (5 _{dec})
[▶_49]	1600:01	SubIndex 001	RO	0x0000:00, 2
	1600:02	SubIndex 002	RO	0x7000:03, 1
	1600:03	SubIndex 003	RO	0x7000:04, 1
	1600:04	SubIndex 004	RO	0x0000:00, 12
	1600:05	SubIndex 005	RO	0x7000:11, 32
1601:0	Subindex	CNT RxPDO-Map Outputs Ch.2	RO	0x05 (5 _{dec})
[<u>50]</u>	1601:01	SubIndex 001	RO	0x0000:00, 2
	1601:02	SubIndex 002	RO	0x7010:03, 1
	1601:03	SubIndex 003	RO	0x7010:04, 1
	1601:04	SubIndex 004	RO	0x0000:00, 12
	1601:05	SubIndex 005	RO	0x7010:11, 32
<u>1A00:0</u>	Subindex	CNT TxPDO-Map InputsCh.1	RO	0x0A (10 _{dec})
[<u>50]</u>	1A00:01	SubIndex 001	RO	0x0000:00, 2
	1A00:02	SubIndex 002	RO	0x6000:03, 1
	1A00:03	SubIndex 003	RO	0x6000:04, 1
	1A00:04	SubIndex 004	RO	0x6000:05, 1
	1A00:05	SubIndex 005	RO	0x6000:06, 1
	1A00:06	SubIndex 006	RO	0x0000:00, 7
	1A00:07	SubIndex 007	RO	0x6000:0E, 1
	1A00:08	SubIndex 008	RO	0x6000:0F, 1
	1A00:09	SubIndex 009	RO	0x6000:10, 1
	1A00:0A	SubIndex 010	RO	0x6000:11, 32

BECKHOFF

Index (h	ex)	Name	Flags	Default value
1A01:0	Subindex	CNT TxPDO-Map Inputs Ch.2	RO	0x0A (10 _{dec})
[▶ 50]	1A01:01	SubIndex 001	RO	0x0000:00, 2
	1A01:02	SubIndex 002	RO	0x6010:03, 1
	1A01:03	SubIndex 003	RO	0x6010:04, 1
	1A01:04	SubIndex 004	RO	0x6010:05, 1
	1A01:05	SubIndex 005	RO	0x6010:06, 1
	1A01:06	SubIndex 006	RO	0x0000:00, 7
	1A01:07	SubIndex 007	RO	0x6010:0E, 1
	1A01:08	SubIndex 008	RO	0x6010:0F, 1
	1A01:09	SubIndex 009	RO	0x6010:10, 1
	1A01:0A	SubIndex 010	RO	0x6010:11, 32
1A02:0	Subindex	DIG TxPDO-Map Inputs	RO	0x09 (9 _{dec})
[<u>] 51]</u>	1A02:01	SubIndex 001	RO	0x6020:01, 1
	1A02:02	SubIndex 002	RO	0x6020:02, 1
	1A02:03	SubIndex 003	RO	0x6020:03, 1
	1A02:04	SubIndex 004	RO	0x6020:04, 1
	1A02:05	SubIndex 005	RO	0x6020:05, 1
	1A02:06	SubIndex 006	RO	0x6020:06, 1
	1A02:07	SubIndex 007	RO	0x6020:07, 1
	1A02:08	SubIndex 008	RO	0x6020:08, 1
	1A02:09	SubIndex 009	RO	0x0000:00, 8
1A03:0	Subindex	DIG TxPDO-Map Inputs Device	RO	0x07 (7 _{dec})
[▶ <u>51]</u>	1A03:01	SubIndex 001	RO	0x0000:00, 1
	1A03:02	SubIndex 002	RO	0xF600:02, 1
	1A03:03	SubIndex 003	RO	0xF600:03, 1
	1A03:04	SubIndex 004	RO	0x0000:00, 10
	1A03:05	SubIndex 005	RO	0xF600:0E, 1
	1A03:06	SubIndex 006	RO	0xF600:0F, 1
	1A03:07	SubIndex 007	RO	0xF600:10, 1
<u>1C00:0</u>	Subindex	Sync manager type	RO	0x04 (4 _{dec})
[<u>• 51]</u>	1C00:01	SubIndex 001	RO	0x01 (1 _{dec})
	1C00:02	SubIndex 002	RO	0x02 (2 _{dec})
	1C00:03	SubIndex 003	RO	0x03 (3 _{dec})
	1C00:04	SubIndex 004	RO	0x04 (4 _{dec})
<u>1C12:0</u>	Subindex	RxPDO assign	RW	0x02 (2 _{dec})
[▶ <u>51]</u>	1C12:01	SubIndex 001	RW	0x1600 (5632 _{dec})
	1C12:02	SubIndex 002	RW	0x1601 (5633 _{dec})
<u>1C13:0</u>	Subindex	TxPDO assign	RW	0x04 (4 _{dec})
[<u>▶ 52]</u>	1C13:01	SubIndex 001	RW	0x1A00 (6656 _{dec})
	1C13:02	SubIndex 002	RW	0x1A01 (6657 _{dec})
	1C13:03	SubIndex 003	RW	0x1A02 (6658 _{dec})
	1C13:04	SubIndex 004	RW	0x1A03 (6659 _{dec})

Index (h	ex)	Name	Flags	Default value
1C32:0	Subindex	SM output parameter	RO	0x20 (32 _{dec})
[<u>}_52]</u>	1C32:01	Sync mode	RW	0x0001 (1 _{dec})
	1C32:02	Cycle time	RW	0x000F4240 (1000000 _{dec})
	1C32:03	Shift time	RO	0x0000000 (0 _{dec})
	1C32:04	Sync modes supported	RO	0xC007 (49159 _{dec})
	1C32:05	Minimum cycle time	RO	0x0003D090 (250000 _{dec})
	1C32:06	Calc and copy time	RO	0x0000000 (0 _{dec})
	1C32:07	Minimum delay time	RO	0x00000000 (0 _{dec})
	1C32:08	Command	RW	0x0000 (0 _{dec})
	1C32:09	Maximum delay time	RO	0x00000000 (0 _{dec})
	1C32:0B	SM event missed counter	RO	0x0000 (0 _{dec})
	1C32:0C	Cycle exceeded counter	RO	0x0000 (0 _{dec})
	1C32:0D	Shift too short counter	RO	0x0000 (0 _{dec})
	1C32:20	Sync error	RO	0x00 (0 _{dec})
1C33:0	Subindex	SM input parameter	RO	0x20 (32 _{dec})
[<u>\$53]</u>	1C33:01	Sync mode	RW	0x0022 (34 _{dec})
1,1	1C33:02	Cycle time	RW	0x000F4240 (1000000 _{dec})
	1C33:03	Shift time	RO	0x00000000 (0 _{dec})
	1C33:04	Sync modes supported	RO	0xC007 (49159 _{dec})
	1C33:05	Minimum cycle time	RO	0x0003D090 (250000 _{dec})
	1C33:06	Calc and copy time	RO	0x00000000 (0 _{dec})
	1C33:07	Minimum delay time	RO	0x00000000 (0 _{dec})
	1C33:08	Command	RW	0x0000 (0 _{dec})
	1C33:09	Maximum delay time	RO	0x000000000000000000000000000000000000
	1C33:0B	SM event missed counter	RO	0x0000 (0 _{dec})
	1C33:0C	Cycle exceeded counter	RO	0x0000 (0 _{dec})
	1C33:0D	Shift too short counter	RO	0x0000 (0 _{dec})
	1C33:20	Sync error	RO	0x00 (0 _{dec})
6000:0	Subindex	CNT Inputs	RO	0x11 (17 _{dec})
<u>5000.0</u> [▶ <u>_54]</u>	6000:03	Set counter done	RO	0x00 (0 _{dec})
1, 741	6000:04	Counter inhibited	RO	0x00 (0 _{dec})
	6000:05	Status of input UD	RO	0x00 (0 _{dec})
	6000:06	Status of input clock	RO	0x00 (0 _{dec})
	6000:0E	Sync error	RO	0x00 (0 _{dec})
	6000:0E	TxPDO State	RO	0x00 (0 _{dec})
	6000:10	TxPDO Toggle	RO	0x00 (0 _{dec})
	6000:10	Counter value	RO	0x00000000 (0 _{dec})
6010:0	Subindex	CNT Inputs	RO	0x11 (17 _{dec})
<u>6010.0</u> [▶ <u>54]</u>	6010:03	Set counter done	RO	0x00 (0 _{dec})
1, 741	6010:04	Counter inhibited	RO	0x00 (0 _{dec})
	6010:04	Status of input UD	RO	0x00 (0 _{dec})
	6010:05	Status of input clock	RO	0x00 (0 _{dec})
	6010:08	Sync error	RO	
		-	RO	$0 \times 00 (0_{dec})$
	6010:0F	TxPDO State		$0x00(0_{dec})$
	6010:10	TxPDO Toggle	RO	0x00 (0 _{dec})
	6010:11	Counter value	RO	0x0000000 (0 _{dec})

Index (h	ex)	Name	Flags	Default value
6020:0	Subindex	DIG Inputs	RO	0x08 (8 _{dec})
[<u>• 54]</u>	6020:01	Input 0	RO	0x00 (0 _{dec})
	6020:02	Input 1	RO	0x00 (0 _{dec})
	6020:03	Input 2	RO	0x00 (0 _{dec})
	6020:04	Input 3	RO	0x00 (0 _{dec})
	6020:05	Input 4	RO	0x00 (0 _{dec})
	6020:06	Input 5	RO	0x00 (0 _{dec})
	6020:07	Input 6	RO	0x00 (0 _{dec})
	6020:08	Input 7	RO	0x00 (0 _{dec})
7000:0	Subindex	CNT Outputs	RO	0x11 (17 _{dec})
▶ <u>54]</u>	7000:03	Set counter	RO	0x00 (0 _{dec})
- <u></u>	7000:04	Inhibit counter	RO	0x00 (0 _{dec})
	7000:11	Set counter value	RO	0x00000000 (0 _{dec})
7010:0	Subindex	CNT Outputs	RO	0x11 (17 _{dec})
[<u>55]</u>	7010:03	Set counter	RO	0x00 (0 _{dec})
. <u> </u>	7010:04	Inhibit counter	RO	$0 \times 00 (0_{dec})$
	7010:11	Set counter value	RO	0x00000000 (0 _{dec})
8000:0	Subindex	CNT Settings	RW	0x13 (19 _{dec})
[▶ <u>48]</u>	8000:03	Enable reload	RW	0x00 (0 _{dec})
	8000:04	Count down	RW	0x00 (0 _{dec})
	8000:05	Operating mode	RW	0x01 (1 _{dec})
	8000:13	Counter reload value	RW	0x00000000 (0 _{dec})
8010:0	Subindex	CNT Settings	RW	0x13 (19 _{dec})
[▶ <u>48]</u>	8010:03	Enable reload	RW	0x00 (0 _{dec})
<u></u>	8010:04	Count down	RW	0x00 (0 _{dec})
	8010:05	Operating mode	RW	0x01 (1 _{dec})
	8010:13	Counter reload value	RW	0x00000000 (0 _{dec})
8022:0	Subindex	DIG Filter Settings	RW	0x08 (8 _{dec})
<u>5022.0</u> [▶ <u>48]</u>	8022:01	Input 0	RW	0x00 (0 _{dec})
<u>, 101</u>	8022:02	Input 1	RW	0x00 (0 _{dec})
	8022:02	Input 2	RW	0x00 (0 _{dec})
	8022:04	Input 3	RW	0x00 (0 _{dec})
	8022:04	Input 4	RW	0x00 (0 _{dec})
	8022:06	Input 5	RW	0x00 (0 _{dec})
	8022:00	Input 6	RW	0x00 (0 _{dec})
	8022:08	Input 7	RW	0x00 (0 _{dec})
-000.0	Subindex	Modular device profile	RO	0x02 (2 _{dec})
F000:0	F000:01	Module index distance	RO	0x0010 (16 _{dec})
▶ <u>55]</u>	F000:01	Maximum number of modules	RO	0x0003 (3 _{dec})
		Code word	RW	0x00003 (3 _{dec})
F008 [▶ 5				
F010:0	Subindex	Module list	RW	0x03 (3 _{dec})
[▶ <u>55]</u>	F010:01	SubIndex 001	RW	0x0000096 (150 _{dec})
	F010:02	SubIndex 002	RW	0x0000096 (150 _{dec})
	F010:03	SubIndex 003	RW	0x00000118 (280 _{dec})

Index (h	ex)	Name	Flags	Default value
F600:0	Subindex	DIG Inputs	RO	0x10 (16 _{dec})
[<u>55]</u>	F600:02	Error channel 1	RO	0x00 (0 _{dec})
	F600:03	Error channel 2	RO	0x00 (0 _{dec})
	F600:0E	Sync error	RO	0x00 (0 _{dec})
	F600:0F	TxPDO State	RO	0x00 (0 _{dec})
	F600:10	TxPDO Toggle	RO	0x00 (0 _{dec})

Key

Flags:

RO (Read Only): this object can be read only RW (Read/Write): this object can be read and written to

5.5.2 Object description and parameterization



Parameterization

The terminal is parameterized via the CoE - Online tab (double-click on the respective object) or via the Process Data tab (assignment of PDOs).

EtherCAT XML Device Description

The display matches that of the CoE objects from the EtherCAT XML Device Description. We recommend downloading the latest XML file from the download area on the Beckhoff website (http:// www.beckhoff.de/german/default.htm?download/elconfg.htm) and installing it according to the installation instructions.

Introduction

The CoE overview contains objects for different intended applications:

- <u>Objects required for parameterization [▶ 47]</u> during commissioning
- Objects intended for regular operation, e.g. through ADS access.
- Objects for indicating internal settings (may be fixed)
- Further profile-specific objects [> 54] indicating inputs, outputs and status information

The following section first describes the objects required for normal operation, followed by a complete overview of missing objects.

Additional objects

5.5.2.1 Objects to be parameterized during commissioning

Index 1011 Restore default parameters

Index (hex)	Name	Meaning	Data type	Flags	Default
1011:0	Restore default pa- rameters	Restore default parameters	UINT8	RO	0x01 (1 _{dec})
1011:01		If this object is set to " 0x64616F6C" in the set value dia- log, all backup objects are reset to their delivery state.	UINT32	RW	0x0000000 (0 _{dec})

Index 8000 CNT Settings

Index (hex)	Name	Meaning		Data type	Flags	Default
8000:0	CNT Settings	Maximum	subindex	UINT8	RO	0x13 (19 _{dec})
8000:03	Enable reload	The counte [<u>48]</u>	er counts to the value in index 0x8000:13	BOOLEAN	RW	0x00 (0 _{dec})
8000:04	Count down	Counting d	irection:	BOOLEAN	RW	0x00 (0 _{dec})
		0 _{bin}	Forward			
		1 _{bin}	Down			
8000:05	Operating mode	Operation	mode	BIT2	RW	0x01 (1 _{dec})
		01 _{bin}	Enable pos. gate (gate inhibits with positive level)			
		10 _{bin}	Enable neg. gate (gate inhibits with negative level)			
8000:13	Counter reload value	0x8000:03 If counting and, on ex downward,	at can be activated via "Enable reload" (index [<u>48]</u>). upward, the counter counts up to this limit ceeding it, starts again from zero. If counting the counter counts down to 0 and, on falling reloaded with the value from this register.	UINT32	RW	0x0000000 (0 _{dec})

Index 8010 CNT Settings

Index (hex)	Name	Meaning		Data type	Flags	Default
8010:0	CNT Settings	Maximum	subindex	UINT8	RO	0x13 (19 _{dec})
8010:03	Enable reload	The count [<u>48]</u>	er counts to the value in index 0x8010:13	BOOLEAN	RW	0x00 (0 _{dec})
8010:04	Count down	Counting	direction:	BOOLEAN	RW	0x00 (0 _{dec})
		0 _{bin}	Forward	1		
		1 _{bin}	Down			
8010:05	Operating mode	Operation	mode	BIT2	RW	0x01 (1 _{dec})
		01 _{bin}	Enable pos. gate (gate inhibits with positive level)			
		10 _{bin}	Enable neg. gate (gate inhibits with negative level)	-		
8010:13	Counter reload value	0x8010:03 If counting and, on ea downward	hat can be activated via "Enable reload" (<u>index</u> [) 48]). g upward, the counter counts up to this limit (ceeding it, starts again from zero. If counting d, the counter counts down to 0 and, on falling is reloaded with the value from this register.	UINT32	RW	0x00000000 (0 _{dec})

Index 8022 DIG Filter Settings

Index (hex)	Name	Meaning		Data type	Flags	Default
8022:0	DIG Filter Settings	Maximum	subindex	UINT8	RO	0x08 (8 _{dec})
8022:01	Input 0	Filter time	for the input	UINT8	RW	0x00 (0 _{dec})
		0 _{dec}	10 µs			
		1 _{dec}	200 µs			
		2 _{dec}	1 ms			
		3 _{dec}	3 ms			
		4 _{dec}	10 ms			
		5 _{dec}	100 ms			
8022:02	Input 1	see <u>0x8022</u>	2:01 [▶ 48]	UINT8	RW	0x00 (0 _{dec})
8022:03	Input 2	see <u>0x802</u> 2	2:01 [▶ <u>48]</u>	UINT8	RW	0x00 (0 _{dec})
8022:04	Input 3	see <u>0x8022</u>	<u>2:01 [▶ 48]</u>	UINT8	RW	0x00 (0 _{dec})
8022:05	Input 4	see <u>0x8022</u>	<u>2:01 [▶ 48]</u>	UINT8	RW	0x00 (0 _{dec})
8022:06	Input 5	see <u>0x802</u> 2	<u>2:01 [▶ 48]</u>	UINT8	RW	0x00 (0 _{dec})
8022:07	Input 6	see <u>0x8022</u>	<u>2:01 [▶ 48]</u>	UINT8	RW	0x00 (0 _{dec})
8022:08	Input 7	see <u>0x8022</u>	2:01 [▶_48]	UINT8	RW	0x00 (0 _{dec})

5.5.2.2 Standard objects (0x1000-0x1FFF)

The standard objects have the same meaning for all EtherCAT slaves.

Index 1000 Device type

Index (hex)	Name	Meaning	Data type	Flags	Default
1000:0		Device type of the EtherCAT slave: The Lo-Word con- tains the CoE profile used (5001). The Hi-Word contains the module profile according to the modular device pro- file.	UINT32	-	0x00001389 (5001 _{dec})

Index 1008 Device name

Index (hex)	Name	Meaning	Data type	Flags	Default
1008:0	Device name	Device name of the EtherCAT slave	STRING	RO	EP1518-0002

Index 1009 Hardware version

Index (hex)	Name	Meaning	Data type	Flags	Default
1009:0	Hardware version	Hardware version of the EtherCAT slave	STRING	RO	00

Index 100A Software Version

Index (hex)	Name	Meaning	Data type	Flags	Default
100A:0	Software version	Firmware version of the EtherCAT slave	STRING	RO	01.03

Index 1018 Identity

Index (hex)	Name	Meaning	Data type	Flags	Default
1018:0	Identity	Information for identifying the slave	UINT8	RO	0x04 (4 _{dec})
1018:01	Vendor ID	Vendor ID of the EtherCAT slave	UINT32	RO	0x0000002 (2 _{dec})
1018:02	Product code	Product code of the EtherCAT slave	UINT32	RO	0x05EE4052 (99500114 _{dec})
1018:03		Revision number of the EtherCAT slave; the low word (bit 0-15) indicates the special terminal number, the high word (bit 16-31) refers to the device description	UINT32	RO	0x00100002 (1048578 _{dec})
1018:04		Serial number of the EtherCAT slave; the low byte (bit 0-7) of the low word contains the year of production, the high byte (bit 8-15) of the low word contains the week of production, the high word (bit 16-31) is 0	UINT32	RO	0x0000000 (0 _{dec})

Index 10F0 Backup parameter handling

Index (hex)	Name	Meaning	Data type	Flags	Default
10F0:0		Information for standardized loading and saving of backup entries	UINT8	RO	0x01 (1 _{dec})
10F0:01	Checksum	Checksum across all backup entries of the EtherCAT slave	UINT32	1	0x0000000 (0 _{dec})

Index 1600 CNT RxPDO-Map Outputs Ch.1

Index (hex)	Name	Meaning	Data type	Flags	Default
1600:0	CNT RxPDO-Map Outputs Ch.1	PDO Mapping RxPDO 1	UINT8	RO	0x05 (5 _{dec})
1600:01	SubIndex 001	1. PDO Mapping entry (object 0x7040 (DO Outputs), en- try 0x01 (Output 0))	UINT32	RO	0x0000:00, 2
1600:02	SubIndex 002	2. PDO Mapping entry (15 bits align)	UINT32	RO	0x7000:03, 1
1600:03	SubIndex 003	3. PDO Mapping entry (object 0x7010 (CNT Outputs), entry 0x03 (Set counter))	UINT32	RO	0x7000:04, 1
1600:04	SubIndex 004	4. PDO Mapping entry (object 0x7010 (CNT Outputs), entry 0x04 (Inhibit counter))	UINT32	RO	0x0000:00, 12
1600:05	SubIndex 005	5. PDO Mapping entry (12 bits align)	UINT32	RO	0x7000:11, 32

Index 1601 CNT RxPDO-Map OutputsCh.2

Index (hex)	Name	Meaning	Data type	Flags	Default
1601:0	CNT RxPDO-Map Outputs Ch.2	PDO Mapping RxPDO 2	UINT8	RO	0x05 (5 _{dec})
1601:01	SubIndex 001	1. PDO Mapping entry (object 0x7020 (CNT Outputs), entry 0x01 (Enable output functions))	UINT32	RO	0x0000:00, 2
1601:02	SubIndex 002	2. PDO Mapping entry (object 0x7020 (CNT Outputs), entry 0x02 (Set output))	UINT32	RO	0x7010:03, 1
1601:03	SubIndex 003	3. PDO Mapping entry (object 0x7020 (CNT Outputs), entry 0x03 (Set counter))	UINT32	RO	0x7010:04, 1
1601:04	SubIndex 004	4. PDO Mapping entry (object 0x7020 (CNT Outputs), entry 0x04 (Inhibit counter))	UINT32	RO	0x0000:00, 12
1601:05	SubIndex 005	5. PDO Mapping entry (12 bits align)	UINT32	RO	0x7010:11, 32

Index 1A00 CNT TxPDO-Map InputsCh.1

Index (hex)	Name	Meaning	Data type	Flags	Default
1A00:0	CNT TxPDO-Map In- putsCh.1	PDO Mapping TxPDO 1	UINT8	RO	0x0A (10 _{dec})
1A00:01	SubIndex 001	1. PDO Mapping entry (object 0x6000 (DI Inputs Ch.1), entry 0x01 (Input 0))	UINT32	RO	0x0000:00, 2
1A00:02	SubIndex 002	2. PDO Mapping entry (object 0x6000 (DI Inputs Ch.1), entry 0x02 (Input 1))	UINT32	RO	0x6000:03, 1
1A00:03	SubIndex 003	3. PDO Mapping entry (object 0x6000 (DI Inputs Ch.1), entry 0x03 (Input 2))	UINT32	RO	0x6000:04, 1
1A00:04	SubIndex 004	4. PDO Mapping entry (object 0x6000 (DI Inputs Ch.1), entry 0x04 (Input 3))	UINT32	RO	0x6000:05, 1
1A00:05	SubIndex 005	5. PDO Mapping entry (object 0x6000 (DI Inputs Ch.1), entry 0x05 (Input 4))	UINT32	RO	0x6000:06, 1
1A00:06	SubIndex 006	6. PDO Mapping entry (object 0x6000 (DI Inputs Ch.1), entry 0x06 (Input 5))	UINT32	RO	0x0000:00, 7
1A00:07	SubIndex 007	7. PDO Mapping entry (object 0x6000 (DI Inputs Ch.1), entry 0x06 (Input 6))	UINT32	RO	0x6000:0E, 1
1A00:08	SubIndex 008	8. PDO Mapping entry (object 0x6000 (DI Inputs Ch.1), entry 0x08 (Input 7))	UINT32	RO	0x6000:0F, 1
1A00:09	SubIndex 009	9. PDO Mapping entry (8 bits align)	UINT32	RO	0x6000:10, 1
1A00:0A	SubIndex 010	10. PDO Mapping entry (object 0x6000 (CNT Inputs), en- try 0x11 (Counter value))	UINT32	RO	0x6000:11, 32

Index 1A01 CNT TxPDO-Map InputsCh.2

Index (hex)	Name	Meaning	Data type	Flags	Default
1A01:0	CNT TxPDO-Map In- puts Ch.2	PDO Mapping TxPDO 2	UINT8	RO	0x0A (10 _{dec})
1A01:01	SubIndex 001	1. PDO Mapping entry (15 bits align)	UINT32	RO	0x0000:00, 2
1A01:02	SubIndex 002	2. PDO Mapping entry (object 0x6020 (CNT Inputs), en- try 0x10 (TxPDO Toggle))	UINT32	RO	0x6010:03, 1
1A01:03	SubIndex 003	3. PDO Mapping entry (object 0x6020 (CNT Inputs), entry 0x11 (Counter value))	UINT32	RO	0x6010:04, 1
1A01:04	SubIndex 004	4. PDO Mapping entry (object 0x6020 (CNT Inputs), en- try 0x04 (Counter inhibited))	UINT32	RO	0x6010:05, 1
1A01:05	SubIndex 005	5. PDO Mapping entry (object 0x6020 (CNT Inputs), en- try 0x05 (Status of input UD))	UINT32	RO	0x6010:06, 1
1A01:06	SubIndex 006	6. PDO Mapping entry (object 0x6020 (CNT Inputs), en- try 0x06 (Status of input clock))	UINT32	RO	0x0000:00, 7
1A01:07	SubIndex 007	7. PDO Mapping entry (8 bits align)	UINT32	RO	0x6010:0E, 1
1A01:08	SubIndex 008	8. PDO Mapping entry (object 0x6020 (CNT Inputs), en- try 0x0F (TxPDO State))	UINT32	RO	0x6010:0F, 1
1A01:09	SubIndex 009	9. PDO Mapping entry (object 0x6020 (CNT Inputs), en- try 0x10 (TxPDO Toggle))	UINT32	RO	0x6010:10, 1
1A01:0A	SubIndex 010	10. PDO Mapping entry (object 0x6010 (CNT Inputs), en- try 0x11 (Counter value))	UINT32	RO	0x6010:11, 32

Index 1A02 DIG TxPDO-Map Inputs

Index (hex)	Name	Meaning	Data type	Flags	Default
1A02:0	DIG TxPDO-Map In- puts	PDO Mapping TxPDO 3	UINT8	RO	0x09 (9 _{dec})
1A02:01	SubIndex 001	1. PDO Mapping entry (2 bits align)	UINT32	RO	0x6020:01, 1
1A02:02	SubIndex 002	2. PDO Mapping entry (object 0x6030 (ENC Inputs), en- try 0x03 (Set counter done))	UINT32	RO	0x6020:02, 1
1A02:03	SubIndex 003	3. PDO Mapping entry (object 0x6030 (ENC Inputs), en- try 0x04 (Counter underflow))	UINT32	RO	0x6020:03, 1
1A02:04	SubIndex 004	4. PDO Mapping entry (object 0x6030 (ENC Inputs), en- try 0x05 (Counter overflow))	UINT32	RO	0x6020:04, 1
1A02:05	SubIndex 005	5. PDO Mapping entry (3 bits align)	UINT32	RO	0x6020:05, 1
1A02:06	SubIndex 006	6. PDO Mapping entry (object 0x6030 (ENC Inputs), en- try 0x09 (Status of input A))	UINT32	RO	0x6020:06, 1
1A02:07	SubIndex 007	7. PDO Mapping entry (object 0x6030 (ENC Inputs), en- try 0x0A (Status of input B))	UINT32	RO	0x6020:07, 1
1A02:08	SubIndex 008	8. PDO Mapping entry (object 0x6030 (ENC Inputs), en- try 0x0B (Status of input C))	UINT32	RO	0x6020:08, 1
1A02:09	SubIndex 009	9. PDO Mapping entry (4 bits align)	UINT32	RO	0x0000:00, 8

Index 1A03 DIG TxPDO-Map Inputs Device

Index (hex)	Name	Meaning	Data type	Flags	Default
1A03:0	DIG TxPDO-Map In- puts Device	PDO Mapping TxPDO 4	UINT8	RO	0x07 (7 _{dec})
1A03:01	SubIndex 001	1. PDO Mapping entry (1 bits align)	UINT32	RO	0x0000:00, 1
1A03:02	SubIndex 002	2. PDO Mapping entry (object 0xF600 (DIG Inputs), entry 0x02 (Error channel 1))	UINT32	RO	0xF600:02, 1
1A03:03	SubIndex 003	3. PDO Mapping entry (object 0xF600 (DIG Inputs), entry 0x03 (Error channel 2))	UINT32	RO	0xF600:03, 1
1A03:04	SubIndex 004	4. PDO Mapping entry (10 bits align)	UINT32	RO	0x0000:00, 10
1A03:05	SubIndex 005	5. PDO Mapping entry (object 0xF600 (DIG Inputs), entry 0x0E (Sync error))	UINT32	RO	0xF600:0E, 1
1A03:06	SubIndex 006	6. PDO Mapping entry (1 bits align)	UINT32	RO	0xF600:0F, 1
1A03:07	SubIndex 007	7. PDO Mapping entry (object 0xF600 (DIG Inputs), entry 0x10 (TxPDO Toggle))	UINT32	RO	0xF600:10, 1

Index 1C00 Sync manager type

Index (hex)	Name	Meaning	Data type	Flags	Default
1C00:0	Sync manager type	Using the sync managers	UINT8	RO	0x04 (4 _{dec})
1C00:01	SubIndex 001	Sync-Manager Type Channel 1: Mailbox Write	UINT8	RO	0x01 (1 _{dec})
1C00:02	SubIndex 002	Sync-Manager Type Channel 2: Mailbox Read	UINT8	RO	0x02 (2 _{dec})
1C00:03	SubIndex 003	Sync-Manager Type Channel 3: Process Data Write (Outputs)	UINT8	RO	0x03 (3 _{dec})
1C00:04	SubIndex 004	Sync-Manager Type Channel 4: Process Data Read (In- puts)	UINT8	RO	0x04 (4 _{dec})

Index 1C12 RxPDO assign

Index (hex)	Name	Meaning	Data type	Flags	Default
1C12:0	RxPDO assign	PDO Assign Outputs	UINT8	RW	0x02 (2 _{dec})
1C12:01	Subindex 001	1. allocated RxPDO (contains the index of the associated RxPDO mapping object)	UINT16	RW	0x1600 (5632 _{dec})
1C12:02		2. allocated RxPDO (contains the index of the associated RxPDO mapping object)	UINT16	RW	0x1601 (5633 _{dec})

Index 1C13 TxPDO assign

Index (hex)	Name	Meaning	Data type	Flags	Default
1C13:0	TxPDO assign	PDO Assign Inputs	UINT8	RW	0x04 (4 _{dec})
1C13:01	Subindex 001	1. allocated TxPDO (contains the index of the associated TxPDO mapping object)	UINT16	RW	0x1A00 (6656 _{dec})
1C13:02	Subindex 002	2. allocated TxPDO (contains the index of the associated TxPDO mapping object)	UINT16	RW	0x1A01 (6657 _{dec})
1C13:03	Subindex 003	3. allocated TxPDO (contains the index of the associated TxPDO mapping object)	UINT16	RW	0x1A02 (6658 _{dec})
1C13:04	Subindex 004	4. allocated TxPDO (contains the index of the associated TxPDO mapping object)	UINT16	RW	0x1A03 (6659 _{dec})

Index 1C32 SM output parameter

Index (hex)	Name	Meaning	Data type	Flags	Default
1C32:0	SM output parameter	Synchronization parameters for the outputs	UINT8	RO	0x20 (32 _{dec})
1C32:01	Sync mode	0: Free Run	UINT16	RW	0x0001 (1 _{dec})
		1: Synchronous with SM 2 event			
		• 2: DC-Mode - Synchronous with SYNC0 Event			
		3: DC-Mode - Synchronous with SYNC1 event			
1C32:02	Cycle time	Cycle time (in ns):	UINT32	RW	0x000F4240
		Synchronous with SM 2 event: Master cycle time			(100000 _{dec})
		DC mode: SYNC0/SYNC1 Cycle Time			
1C32:03	Shift time	Time between SYNC0 event and output of the outputs (in ns, DC mode only)	UINT32	RO	0x0000000 (0 _{dec})
1C32:04	Sync modes sup-	Supported synchronization modes:	UINT16	RO	0xC007
	ported	Bit 0 = 1: free run is supported			(49159 _{dec})
		• Bit 1 = 1: synchronous with SM 2 event is supported			
		• Bit 2-3 = 01: DC mode is supported			
		 Bit 4-5 = 10: Output shift with SYNC1 event (only DC mode) 			
		 Bit 14 = 1: dynamic times (measurement through writing of <u>0x1C32:08 [* 52]</u>) 			
1C32:05	Minimum cycle time	Minimum cycle time (in ns)	UINT32	RO	0x0003D090 (250000 _{dec})
1C32:06	Calc and copy time	Minimum time between SYNC0 and SYNC1 event (in ns, DC mode only)	UINT32	RO	0x0000000 (0 _{dec})
1C32:07	Minimum delay time	Minimum time between SYNC1 event and output of the outputs (in ns, DC mode only)	UINT32	RO	0x0000000 (0 _{dec})
1C32:08	Command	0: Measurement of the local cycle time is stopped1: Measurement of the local cycle time is started	UINT16	RW	0x0000 (0 _{dec})
		The entries $0x1C32:03 [> 52], 0x1C32:05 [> 52], 0x1C32:06 [> 52], 0x1C32:07 [> 52], 0x1C32:09 [> 52], 0x1C33:03 [> 53], 0x1C33:06 [> 52], 0x1C33:07 [> 52] and 0x1C33:09 [> 53] are updated with the maximum measured values.For a subsequent measurement the measured values are reset$			
1C32:09	Maximum delay time	Minimum time between SYNC1 event and output of the outputs (in ns, DC mode only)	UINT32	RO	0x0000000 (0 _{dec})
1C32:0B	SM event missed counter	Number of missed SM events in OPERATIONAL (DC mode only)	UINT16	RO	0x0000 (0 _{dec})
1C32:0C	Cycle exceeded counter	Number of occasions the cycle time was exceeded in OPERATIONAL (cycle was not completed in time or the next cycle began too early)	UINT16	RO	0x0000 (0 _{dec})
1C32:0D	Shift too short counter	Number of occasions that the interval between SYNC0 and SYNC1 event was too short (DC mode only)	UINT16	RO	0x0000 (0 _{dec})
1C32:20	Sync error	The synchronization was not correct in the last cycle (outputs were output too late; DC mode only)	BOOLEAN	RO	0x00 (0 _{dec})

Index 1C33 SM input parameter

Index (hex)	Name	Meaning	Data type	Flags	Default
1C33:0	SM input parameter	Synchronization parameters for the inputs	UINT8	RO	0x20 (32 _{dec})
1C33:01 Sy	Sync mode	Current synchronization mode:	UINT16	RW	0x0022 (34 _{dec})
		0: Free Run			
		 1: Synchronous with SM 3 Event (no outputs available) 			
		 2: DC - Synchronous with SYNC0 Event 			
		 3: DC - Synchronous with SYNC1 Event 			
		 34: Synchronous with SM 2 event (outputs available) 			
1C33:02	Cycle time	as <u>0x1C32:02</u> [▶ <u>52]</u>	UINT32	RW	0x000F4240 (1000000 _{dec})
1C33:03	Shift time	Time between SYNC0 event and reading of the inputs (in ns, only DC mode)	UINT32	RO	0x0000000 (0 _{dec})
1C33:04	Sync modes sup-	Supported synchronization modes:	UINT16	RO	0xC007
	ported	Bit 0: free run is supported			(49159 _{dec})
		 Bit 1: Synchronous with SM 2 Event is supported (outputs available) 			
		 Bit 1: Synchronous with SM 3 Event is supported (no outputs available) 			
		Bit 2-3 = 01: DC mode is supported			
		 Bit 4-5 = 01: Input shift through local event (outputs available) 			
	 Bit 4-5 = 10: Input shift with SYNC1 event (no outputs available) 				
		 Bit 14 = 1: dynamic times (measurement through writing of <u>0x1C32:08 [▶ 52]</u> or <u>0x1C33:08 [▶ 53]</u>) 			
1C33:05	Minimum cycle time	as <u>0x1C32:05</u> [▶ <u>52]</u>	UINT32	RO	0x0003D090 (250000 _{dec})
1C33:06	Calc and copy time	Time between reading of the inputs and availability of the inputs for the master (in ns, only DC mode)	UINT32	RO	0x0000000 (0 _{dec})
1C33:07	Minimum delay time	as <u>0x1C32:07</u> [▶ <u>52]</u>	UINT32	RO	0x0000000 (0 _{dec})
1C33:08	Command	as <u>0x1C32:08 [▶ 52]</u>	UINT16	RW	0x0000 (0 _{dec})
1C33:09	Maximum delay time	as <u>0x1C32:09</u> [▶ <u>52]</u>	UINT32	RO	0x0000000 (0 _{dec})
1C33:0B	SM event missed counter	as 0x1C32:11	UINT16	RO	0x0000 (0 _{dec})
1C33:0C	Cycle exceeded counter	as 0x1C32:12	UINT16	RO	0x0000 (0 _{dec})
1C33:0D	Shift too short counter	as 0x1C32:13	UINT16	RO	0x0000 (0 _{dec})
1C33:20	Sync error	as 0x1C32:32	BOOLEAN	RO	0x00 (0 _{dec})

5.5.2.3 **Profile-specific objects (0x6000-0xFFFF)**

The profile-specific objects have the same meaning for all EtherCAT slaves that support the profile 5001.

Index 6000 CNT Inputs

Index (hex)	Name	Meaning	Data type	Flags	Default
6000:0	CNT Inputs	Maximum subindex	UINT8	RO	0x11 (17 _{dec})
6000:03	Set counter done	The counter was set	BOOLEAN	RO	0x00 (0 _{dec})
6000:04	Counter inhibited	The counter is stopped for as long as this bit is set	BOOLEAN	RO	0x00 (0 _{dec})
6000:05	Status of input UD	The state of the up/down input	BOOLEAN	RO	0x00 (0 _{dec})
6000:06	Status of input clock	The state of the clock input	BOOLEAN	RO	0x00 (0 _{dec})
6000:0E	Sync error	Synchronization error	BOOLEAN	RO	0x00 (0 _{dec})
6000:0F	TxPDO State	Validity of the data of the associated TxPDO (0 = valid, 1 = invalid)	BOOLEAN	RO	0x00 (0 _{dec})
6000:10	TxPDO Toggle	The TxPDO toggle is toggled by the slave when the data of the associated TxPDO is updated	BOOLEAN	RO	0x00 (0 _{dec})
6000:11	Counter value	Counter value	UINT32	RO	0x00000000 (0 _{dec})

Index 6010 CNT Inputs

Index (hex)	Name	Meaning	Data type	Flags	Default
6010:0	CNT Inputs	Maximum subindex	UINT8	RO	0x11 (17 _{dec})
6010:03	Set counter done	The counter was set	BOOLEAN	RO	0x00 (0 _{dec})
6010:04	Counter inhibited	The counter is stopped for as long as this bit is set	BOOLEAN	RO	0x00 (0 _{dec})
6010:05	Status of input UD	The state of the up/down input	BOOLEAN	RO	0x00 (0 _{dec})
6010:06	Status of input clock	The state of the clock input	BOOLEAN	RO	0x00 (0 _{dec})
6010:0E	Sync error	Synchronization error	BOOLEAN	RO	0x00 (0 _{dec})
6010:0F	TxPDO State	Validity of the data of the associated TxPDO (0 = valid, 1 = invalid)	BOOLEAN	RO	0x00 (0 _{dec})
6010:10	TxPDO Toggle	The TxPDO toggle is toggled by the slave when the data of the associated TxPDO is updated	BOOLEAN	RO	0x00 (0 _{dec})
6010:11	Counter value	Counter value	UINT32	RO	0x0000000 (0 _{dec})

Index 6020 DIG Inputs

Index (hex)	Name	Meaning	Data type	Flags	Default
6020:0	DIG Inputs	Maximum subindex	UINT8	RO	0x08 (8 _{dec})
6020:01	Input 0	Digital input 0	BOOLEAN	RO	0x00 (0 _{dec})
6020:02	Input 1	Digital input 1	BOOLEAN	RO	0x00 (0 _{dec})
6020:03	Input 2	Digital input 2	BOOLEAN	RO	0x00 (0 _{dec})
6020:04	Input 3	Digital input 3	BOOLEAN	RO	0x00 (0 _{dec})
6020:05	Input 4	Digital input 4	BOOLEAN	RO	0x00 (0 _{dec})
6020:06	Input 5	Digital input 5	BOOLEAN	RO	0x00 (0 _{dec})
6020:07	Input 6	Digital input 6	BOOLEAN	RO	0x00 (0 _{dec})
6020:08	Input 7	Digital input 7	BOOLEAN	RO	0x00 (0 _{dec})

Index 7000 CNT Outputs

Index (hex)	Name	Meaning	Data type	Flags	Default
7000:0	CNT Outputs	Maximum subindex	UINT8	RO	0x11 (17 _{dec})
7000:03	Set counter	Set counter value	BOOLEAN	RO	0x00 (0 _{dec})
7000:04	Inhibit counter	The counter is stopped, as long as this bit is active. The previous counter state is retained.	BOOLEAN	RO	0x00 (0 _{dec})
7000:11	Set counter value	This is the counter value to be set via "Set counter" (in- dex <u>0x7000:03</u> [▶ <u>54]</u>).	UINT32	RO	0x0000000 (0 _{dec})

Index 7010 CNT Outputs

Index (hex)	Name	Meaning	Data type	Flags	Default
7010:0	CNT Outputs	Maximum subindex	UINT8	RO	0x11 (17 _{dec})
7010:03	Set counter	Set counter value	BOOLEAN	RO	0x00 (0 _{dec})
7010:04	Inhibit counter	The counter is stopped, as long as this bit is active. The previous counter state is retained.	BOOLEAN	RO	0x00 (0 _{dec})
7010:11	Set counter value	This is the counter value to be set via "Set counter" (index $0x7000:03$ [\blacktriangleright 55]).	UINT32	RO	0x0000000 (0 _{dec})

Index F000 Modular Device Profile

Index (hex)	Name	Meaning	Data type	Flags	Default
F000:0	Modular device profile	General information for the modular device profile	UINT8	RO	0x02 (2 _{dec})
F000:01	Module index dis- tance	Index distance of the objects of the individual channels	UINT16	RO	0x0010 (16 _{dec})
F000:02	Maximum number of modules	Number of channels	UINT16	RO	0x0003 (3 _{dec})

Index F008 Code word

Index (hex)	Name	Meaning	Data type	Flags	Default
F008:0	Code word	reserved	UINT32	RW	0x00000000
					(O _{dec})

Index F010 Module List

Index (hex)	Name	Meaning	Data type	Flags	Default
F010:0	Module list	Maximum subindex	UINT8	RW	0x03 (3 _{dec})
F010:01	SubIndex 001	reserved	UINT32	RW	0x00000096 (150 _{dec})
F010:02	SubIndex 002	reserved	UINT32	RW	0x00000096 (150 _{dec})
F010:03	SubIndex 003	reserved	UINT32	RW	0x00000118 (280 _{dec})

Index F600 DIG Inputs

Index (hex)	Name	Meaning	Data type	Flags	Default
F600:0	DIG Inputs	Maximum subindex	UINT8	RO	0x10 (16 _{dec})
F600:02	Error channel 1	If this bit is set, a short circuit has been detected in the supply voltage to sensor group 1 (inputs 0 - 3).	BOOLEAN	RO	0x00 (0 _{dec})
F600:03	Error channel 2	If this bit is set, a short circuit has been detected in the supply voltage to sensor group 2 (inputs 4 - 7).	BOOLEAN	RO	0x00 (0 _{dec})
F600:0E	Sync error	Synchronization error	BOOLEAN	RO	0x00 (0 _{dec})
F600:0F	TxPDO State	Validity of the data of the associated TxPDO (0 = valid, 1 = invalid)	BOOLEAN	RO	0x00 (0 _{dec})
F600:10	TxPDO Toggle	The TxPDO toggle is toggled by the slave when the data of the associated TxPDO is updated	BOOLEAN	RO	0x00 (0 _{dec})

6 Appendix

6.1 General operating conditions

Protection degrees (IP-Code)

The standard IEC 60529 (DIN EN 60529) defines the degrees of protection in different classes.

1. Number: dust protection and touch guard	Definition
0	Non-protected
1	Protected against access to hazardous parts with the back of a hand. Protected against solid foreign objects of \emptyset 50 mm
2	Protected against access to hazardous parts with a finger. Protected against solid foreign objects of Ø 12.5 mm.
3	Protected against access to hazardous parts with a tool. Protected against solid foreign objects Ø 2.5 mm.
4	Protected against access to hazardous parts with a wire. Protected against solid foreign objects \emptyset 1 mm.
5	Protected against access to hazardous parts with a wire. Dust-protected. Intrusion of dust is not totally prevented, but dust shall not penetrate in a quantity to interfere with satisfactory operation of the device or to impair safety.
6	Protected against access to hazardous parts with a wire. Dust-tight. No intrusion of dust.
2. Number: water* protection	Definition
0	Non-protected
1	Protected against water drops
2	Protected against water drops when enclosure tilted up to 15°.
3	Protected against spraying water. Water sprayed at an angle up to 60° on either side of the ver- tical shall have no harmful effects.
4	Protected against splashing water. Water splashed against the disclosure from any direction shall have no harmful effects
5	Protected against water jets
6	Protected against powerful water jets
7	Protected against the effects of temporary immersion in water. Intrusion of water in quantities causing harmful effects shall not be possible when the enclosure is temporarily immersed in water for 30 min. in 1 m depth.

*) These protection classes define only protection against water.

Chemical Resistance

The Resistance relates to the Housing of the IP67 modules and the used metal parts. In the table below you will find some typical resistance.

Character	Resistance
Steam	at temperatures >100°C: not resistant
Sodium base liquor (ph-Value > 12)	at room temperature: resistant > 40°C: not resistant
Acetic acid	not resistant
Argon (technical clean)	resistant

Key

- resistant: Lifetime several months
- · non inherently resistant: Lifetime several weeks
- not resistant: Lifetime several hours resp. early decomposition

6.2 Accessories

Mounting

Ordering information	Description	Link
ZS5300-0011	Mounting rail	<u>Website</u>

Cables

A complete overview of pre-assembled cables for fieldbus components can be found here.

Ordering information	Description	Link
ZK1090-3xxx-xxxx	EtherCAT cable M8, green	<u>Website</u>
ZK1093-3xxx-xxxx	EtherCAT cable M8, yellow	<u>Website</u>
ZK2000-6xxx-xxxx	Sensor cable M12, 4-pin	<u>Website</u>
ZK2020-3xxx-xxxx	Power cable M8, 4-pin	<u>Website</u>

Labeling material, protective caps

Ordering information	Description
ZS5000-0010	Protective cap for M8 sockets, IP67 (50 pieces)
ZS5000-0020	Protective cap for M12 sockets, IP67 (50 pcs.)
ZS5100-0000	Inscription labels, unprinted, 4 strips of 10
ZS5000-xxxx	Printed inscription labels on enquiry

Tools

Ordering information	Description
ZB8801-0000	Torque wrench for plugs, 0.4…1.0 Nm
ZB8801-0001	Torque cable key for M8 / wrench size 9 for ZB8801-0000
ZB8801-0002	Torque cable key for M12 / wrench size 13 for ZB8801-0000
ZB8801-0003	Torque cable key for M12 field assembly / wrench size 18 for ZB8801-0000

Further accessories

Further accessories can be found in the price list for fieldbus components from Beckhoff and online at <u>https://www.beckhoff.com</u>.

6.3 Version identification of EtherCAT devices

6.3.1 General notes on marking

Designation

A Beckhoff EtherCAT device has a 14-digit designation, made up of

- family key
- type
- version
- revision

Example	Family	Туре	Version	Revision
EL3314-0000-0016	EL terminal (12 mm, non- pluggable connection level)	3314 (4-channel thermocouple terminal)	0000 (basic type)	0016
ES3602-0010-0017	ES terminal (12 mm, pluggable connection level)	3602 (2-channel voltage measurement)	0010 (high- precision version)	0017
CU2008-0000-0000	CU device	2008 (8-port fast ethernet switch)	0000 (basic type)	0000

Notes

- The elements mentioned above result in the **technical designation**. EL3314-0000-0016 is used in the example below.
- EL3314-0000 is the order identifier, in the case of "-0000" usually abbreviated to EL3314. "-0016" is the EtherCAT revision.
- The order identifier is made up of
 - family key (EL, EP, CU, ES, KL, CX, etc.)
 - type (3314)
 - version (-0000)
- The **revision** -0016 shows the technical progress, such as the extension of features with regard to the EtherCAT communication, and is managed by Beckhoff.

In principle, a device with a higher revision can replace a device with a lower revision, unless specified otherwise, e.g. in the documentation.

Associated and synonymous with each revision there is usually a description (ESI, EtherCAT Slave Information) in the form of an XML file, which is available for download from the Beckhoff web site. From 2014/01 the revision is shown on the outside of the IP20 terminals, see Fig. *"EL5021 EL terminal, standard IP20 IO device with batch number and revision ID (since 2014/01)"*.

• The type, version and revision are read as decimal numbers, even if they are technically saved in hexadecimal.

BECKHOFF

6.3.2 Version identification of EP/EPI/EPP/ER/ERI boxes

The serial number/ data code for Beckhoff IO devices is usually the 8-digit number printed on the device or on a sticker. The serial number indicates the configuration in delivery state and therefore refers to a whole production batch, without distinguishing the individual modules of a batch.

Structure of the serial number: KK YY FF HH

KK - week of production (CW, calendar week)

- YY year of production
- FF firmware version
- HH hardware version

Example with serial number 12 06 3A 02:

- 12 production week 12
- 06 production year 2006
- 3A firmware version 3A
- 02 hardware version 02

Exceptions can occur in the **IP67 area**, where the following syntax can be used (see respective device documentation):

Syntax: D ww yy x y z u

D - prefix designation ww - calendar week vv - vear

- x firmware version of the bus PCB
- y hardware version of the bus PCB
- z firmware version of the I/O PCB
- u hardware version of the I/O PCB

Example: D.22081501 calendar week 22 of the year 2008 firmware version of bus PCB: 1 hardware version of bus PCB: 5 firmware version of I/O PCB: 0 (no firmware necessary for this PCB) hardware version of I/O PCB: 1

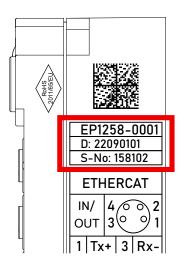


Fig. 22: EP1258-00001 IP67 EtherCAT Box with batch number/DateCode 22090101 and unique serial number 158102

6.3.3 Beckhoff Identification Code (BIC)

The Beckhoff Identification Code (BIC) is increasingly being applied to Beckhoff products to uniquely identify the product. The BIC is represented as a Data Matrix Code (DMC, code scheme ECC200), the content is based on the ANSI standard MH10.8.2-2016.



Fig. 23: BIC as data matrix code (DMC, code scheme ECC200)

The BIC will be introduced step by step across all product groups.

Depending on the product, it can be found in the following places:

- · on the packaging unit
- directly on the product (if space suffices)
- on the packaging unit and the product

The BIC is machine-readable and contains information that can also be used by the customer for handling and product management.

Each piece of information can be uniquely identified using the so-called data identifier (ANSI MH10.8.2-2016). The data identifier is followed by a character string. Both together have a maximum length according to the table below. If the information is shorter, spaces are added to it.

Following information is possible, positions 1 to 4 are always present, the other according to need of production:

Posi- tion	Type of information	Explanation	Data identifier	Number of digits incl. data identifier	Example
1	Beckhoff order number	Beckhoff order number	1P	8	1P072222
2	Beckhoff Traceability Number (BTN)	Unique serial number, see note below	SBTN	12	SBTNk4p562d7
3	Article description	Beckhoff article description, e.g. EL1008	1K	32	1KEL1809
4	Quantity	Quantity in packaging unit, e.g. 1, 10, etc.	Q	6	Q1
5	Batch number	Optional: Year and week of production	2P	14	2P401503180016
6	ID/serial number	Optional: Present-day serial number system, e.g. with safety products	51S	12	<mark>51S</mark> 678294
7	Variant number	Optional: Product variant number on the basis of standard products	30P	32	<mark>30P</mark> F971, 2*K183

Further types of information and data identifiers are used by Beckhoff and serve internal processes.

Structure of the BIC

Example of composite information from positions 1 to 4 and with the above given example value on position 6. The data identifiers are highlighted in bold font:

1P072222SBTNk4p562d71KEL1809 Q1 51S678294

Accordingly as DMC:



Fig. 24: Example DMC 1P072222SBTNk4p562d71KEL1809 Q1 51S678294

BTN

An important component of the BIC is the Beckhoff Traceability Number (BTN, position 2). The BTN is a unique serial number consisting of eight characters that will replace all other serial number systems at Beckhoff in the long term (e.g. batch designations on IO components, previous serial number range for safety products, etc.). The BTN will also be introduced step by step, so it may happen that the BTN is not yet coded in the BIC.

NOTE

This information has been carefully prepared. However, the procedure described is constantly being further developed. We reserve the right to revise and change procedures and documentation at any time and without prior notice. No claims for changes can be made from the information, illustrations and descriptions in this information.

6.3.4 Electronic access to the BIC (eBIC)

Electronic BIC (eBIC)

The Beckhoff Identification Code (BIC) is applied to the outside of Beckhoff products in a visible place. If possible, it should also be electronically readable.

Decisive for the electronic readout is the interface via which the product can be electronically addressed.

K-bus devices (IP20, IP67)

Currently, no electronic storage and readout is planned for these devices.

EtherCAT devices (IP20, IP67)

All Beckhoff EtherCAT devices have a so-called ESI-EEPROM, which contains the EtherCAT identity with the revision number. Stored in it is the EtherCAT slave information, also colloquially known as ESI/XML configuration file for the EtherCAT master. See the corresponding chapter in the EtherCAT system manual (Link) for the relationships.

The eBIC is also stored in the ESI-EEPROM. The eBIC was introduced into the Beckhoff I/O production (terminals, box modules) from 2020; widespread implementation is expected in 2021.

The user can electronically access the eBIC (if existent) as follows:

- With all EtherCAT devices, the EtherCAT master (TwinCAT) can read the eBIC from the ESI-EEPROM
 - From TwinCAT 3.1 build 4024.11, the eBIC can be displayed in the online view.
 - To do this, check the checkbox "Show Beckhoff Identification Code (BIC)" under EtherCAT → Advanced Settings → Diagnostics:

TwinCAT	Project30	4 X									
Genera NetId:		r EtherCAT Online		Online	Advanced S Export Configu Sync Unit As Topolo	ration File		Advanced Settings	Online View 0000 ESC Rev/Type' 0002 ESC Pai/ 0004 SM/TMMU C4' 0005 Port/DPRAM'	^	0000 Add
Fran	ne Cmd) LWF) BRD		Len 1 2	WC 1 2	Sync Unit <default></default>	Cycle (ms) 4.000 4.000	Utilizatio 0.17 0.17	Emergency Diagnosis LOnline View	0008 Features' 0019 Prys. Add' 0012 Configured Sation Alea' 0023 Vaccess Protect' 0023 Vaccess Protect' 0020 ESC Credit 0020 ESC Configured 0120 E		State Changes / Not Present) Show Production Info Show Beckhoff Identification Code(BIC)

• The BTN and its contents are then displayed:

General Adapter EtherCAT Online CoE - Online

No	Addr	Name	State	CRC	Fw	Hw	Production Data	ItemNo	BTN	Description	Quantity	BatchNo	SerialNo
1	1001	Term 1 (EK1100)	OP	0.0	0	0							
2	1002	Tem 2 (EL1018)	OP	0.0	0	0	2020 KW36 Fr	072222	k4p562d7	EL1809	1		678294
3	1003	Term 3 (EL3204)	OP	0.0	7	6	2012 KW24 Sa						
4	1004	Tem 4 (EL2004)	OP	0.0	0	0	-	072223	k4p562d7	EL2004	1		678295
5	1005	Tem 5 (EL1008)	OP	0.0	0	0							
- 6	1006	Tem 6 (EL2008)	OP	0.0	0	12	2014 KW14 Mo						
-7	1007	Term 7 (EK1110)	OP	0	1	8	2012 KW25 Mo						

- Note: as can be seen in the illustration, the production data HW version, FW version and production date, which have been programmed since 2012, can also be displayed with "Show Production Info".
- From TwinCAT 3.1. build 4024.24 the functions FB_EcReadBIC and FB_EcReadBTN for reading into the PLC and further eBIC auxiliary functions are available in the Tc2_EtherCAT Library from v3.3.19.0.
- In the case of EtherCAT devices with CoE directory, the object 0x10E2:01 can additionally by used to display the device's own eBIC; the PLC can also simply access the information here:

• The device must be in PREOP/SAFEOP/OP for access:

Inc	dex	Name	Flags	Value		
	1000	Device type	RO	0x015E1389 (22942601)		
	1008	Device name	RO	ELM3704-0000		
	1009	Hardware version	RO	00		
	100A	Software version	RO	01		
	100B	Bootloader version	RO	J0.1.27.0		
ŧ	1011:0	Restore default parameters	RO	>1<		
•	1018:0	Identity	RO	>4<		
8	10E2:0	Manufacturer-specific Identification C	RO	>1<		
	10E2:01	SubIndex 001	RO	1P158442SBTN0008jekp1KELM3704	Q1	2P482001000016
•	10F0:0	Backup parameter handling	RO	>1<		
•	10F3:0	Diagnosis History	RO	>21 <		
	10F8	Actual Time Stamp	RO	0x170bfb277e		

- the object 0x10E2 will be introduced into stock products in the course of a necessary firmware revision.
- From TwinCAT 3.1. build 4024.24 the functions FB_EcCoEReadBIC and FB_EcCoEReadBTN for reading into the PLC and further eBIC auxiliary functions are available in the Tc2_EtherCAT Library from v3.3.19.0.
- Note: in the case of electronic further processing, the BTN is to be handled as a string(8); the identifier "SBTN" is not part of the BTN.
- Technical background

The new BIC information is additionally written as a category in the ESI-EEPROM during the device production. The structure of the ESI content is largely dictated by the ETG specifications, therefore the additional vendor-specific content is stored with the help of a category according to ETG.2010. ID 03 indicates to all EtherCAT masters that they must not overwrite these data in case of an update or restore the data after an ESI update.

The structure follows the content of the BIC, see there. This results in a memory requirement of approx. 50..200 bytes in the EEPROM.

- · Special cases
 - If multiple, hierarchically arranged ESCs are installed in a device, only the top-level ESC carries the eBIC Information.
 - If multiple, non-hierarchically arranged ESCs are installed in a device, all ESCs carry the eBIC Information.
 - If the device consists of several sub-devices with their own identity, but only the top-level device is accessible via EtherCAT, the eBIC of the top-level device is located in the CoE object directory 0x10E2:01 and the eBICs of the sub-devices follow in 0x10E2:nn.

Profibus/Profinet/DeviceNet... Devices

Currently, no electronic storage and readout is planned for these devices.

6.4 Support and Service

Beckhoff and their partners around the world offer comprehensive support and service, making available fast and competent assistance with all questions related to Beckhoff products and system solutions.

Beckhoff's branch offices and representatives

Please contact your Beckhoff branch office or representative for <u>local support and service</u> on Beckhoff products!

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Support offers you comprehensive technical assistance, helping you not only with the application of individual Beckhoff products, but also with other, wide-ranging services:

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